



# INTERIOR COMPONENT SYSTEM

S-2400/2450

# **SERVICE** Manual

## INTERIOR COMPONENT SYSTEM



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**ELECTRONICS**

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## 2. Disassembly and Reassembly

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### 2-1 Front Cabinet

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1. Remove 13 screws of ① of its rear part.(1 screw of Antenna)
2. Remove Front Cabinet to frontward.

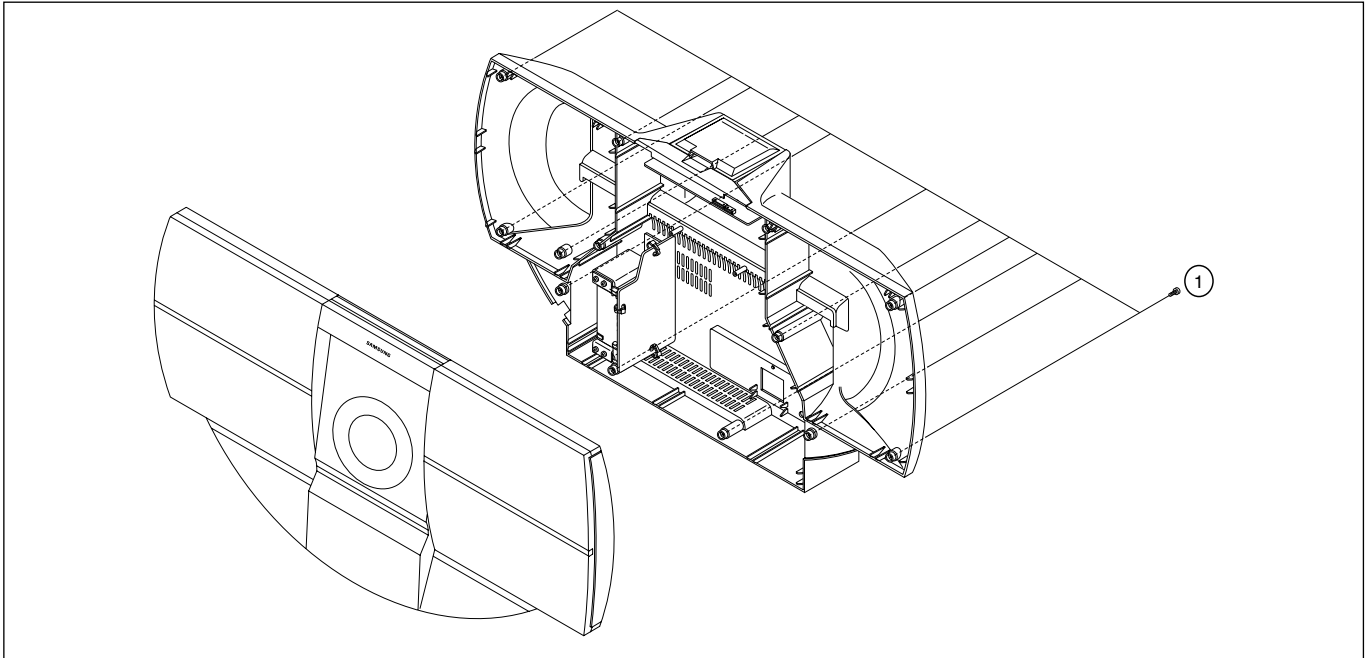


Fig 2-1

### 2-2 Main-PCB ASS'Y AND CD-MECHA ASS'Y

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1. Remove 6 screws of B its rear part
2. Remove the connect wire of C, and remove Main-PCB ASS'Y to backward.
3. Remove 3 screws of A its rear part ,and remove CD-Mecha ASS'Y to backward.

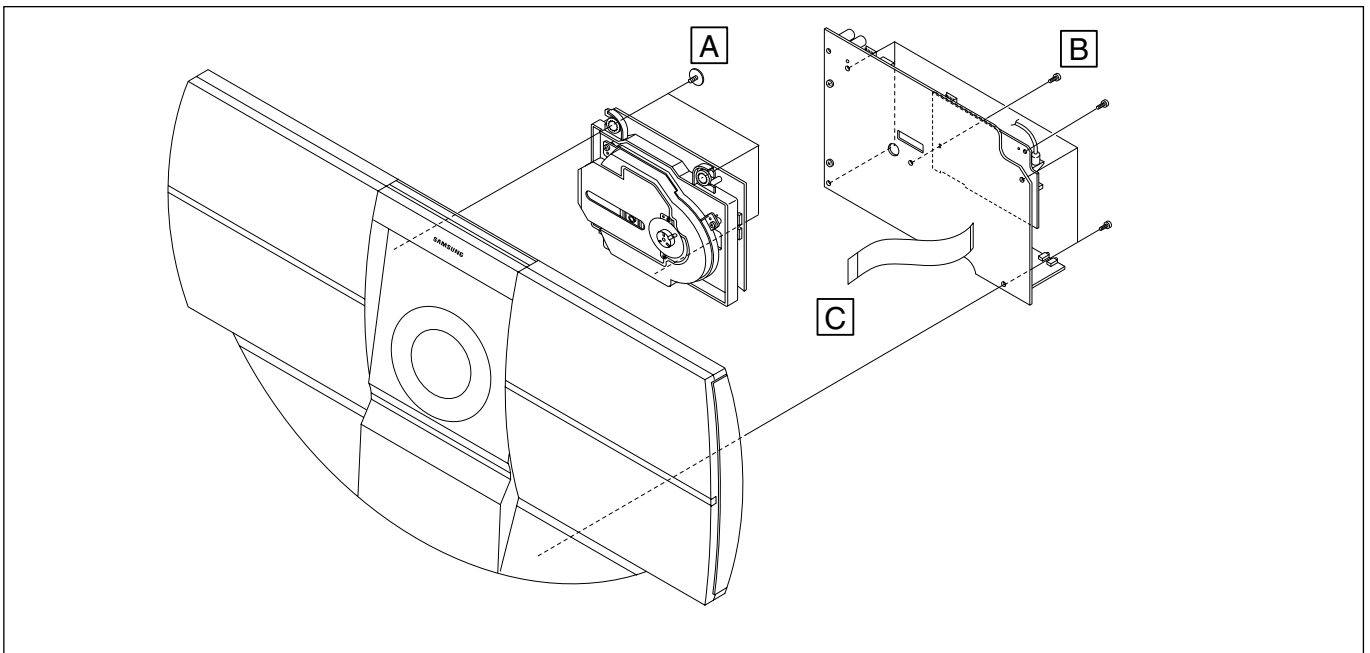


Fig 2-2

## 2-3 Cabinet-Top ASS'Y

1. Remove 2 screws of ② of its rear part.
2. Pull up Cabinet-Top Ass'y to frontward, and remove it

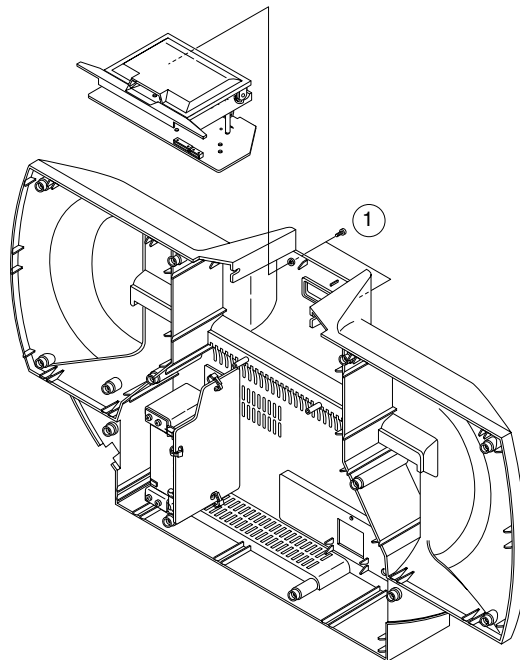


Fig 2-3

## 2-4 CAP Front ASS'Y

1. Remove Tersey-Frame of C
2. Remove 2 screws of A (Deco-Front).
3. Remove 6 screws of B (Cap-Front).
4. Remove Cap-Front Ass'y to frontward.

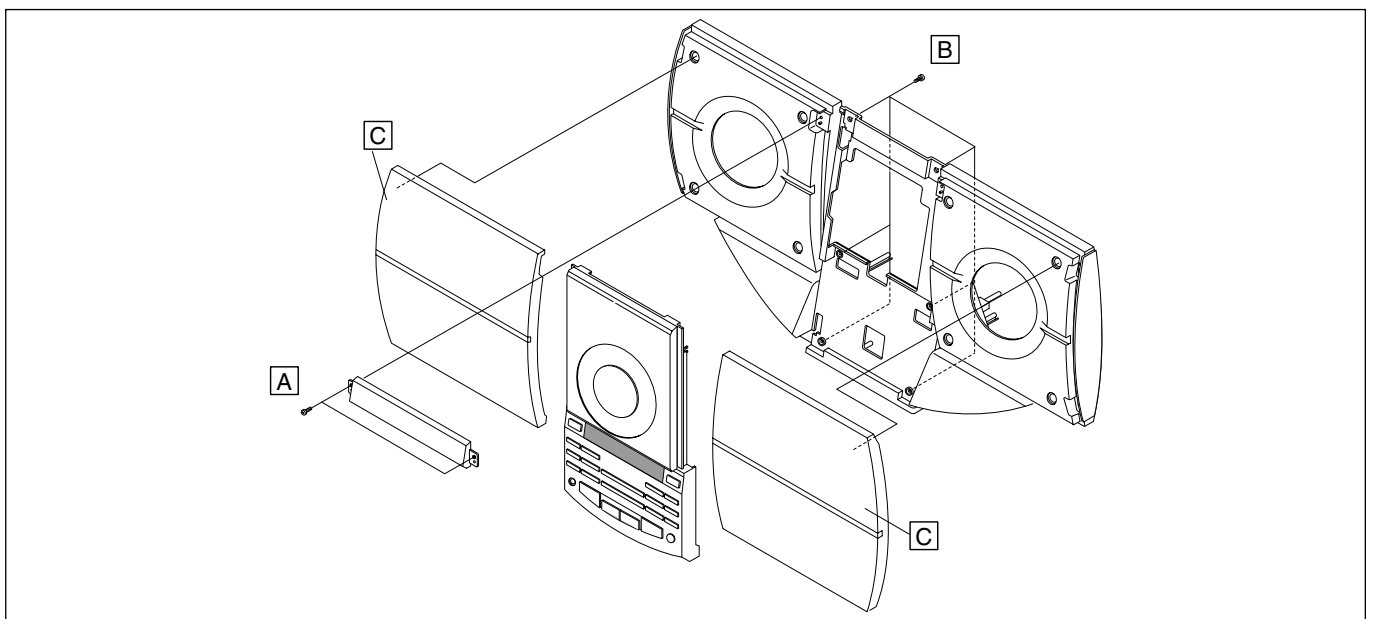
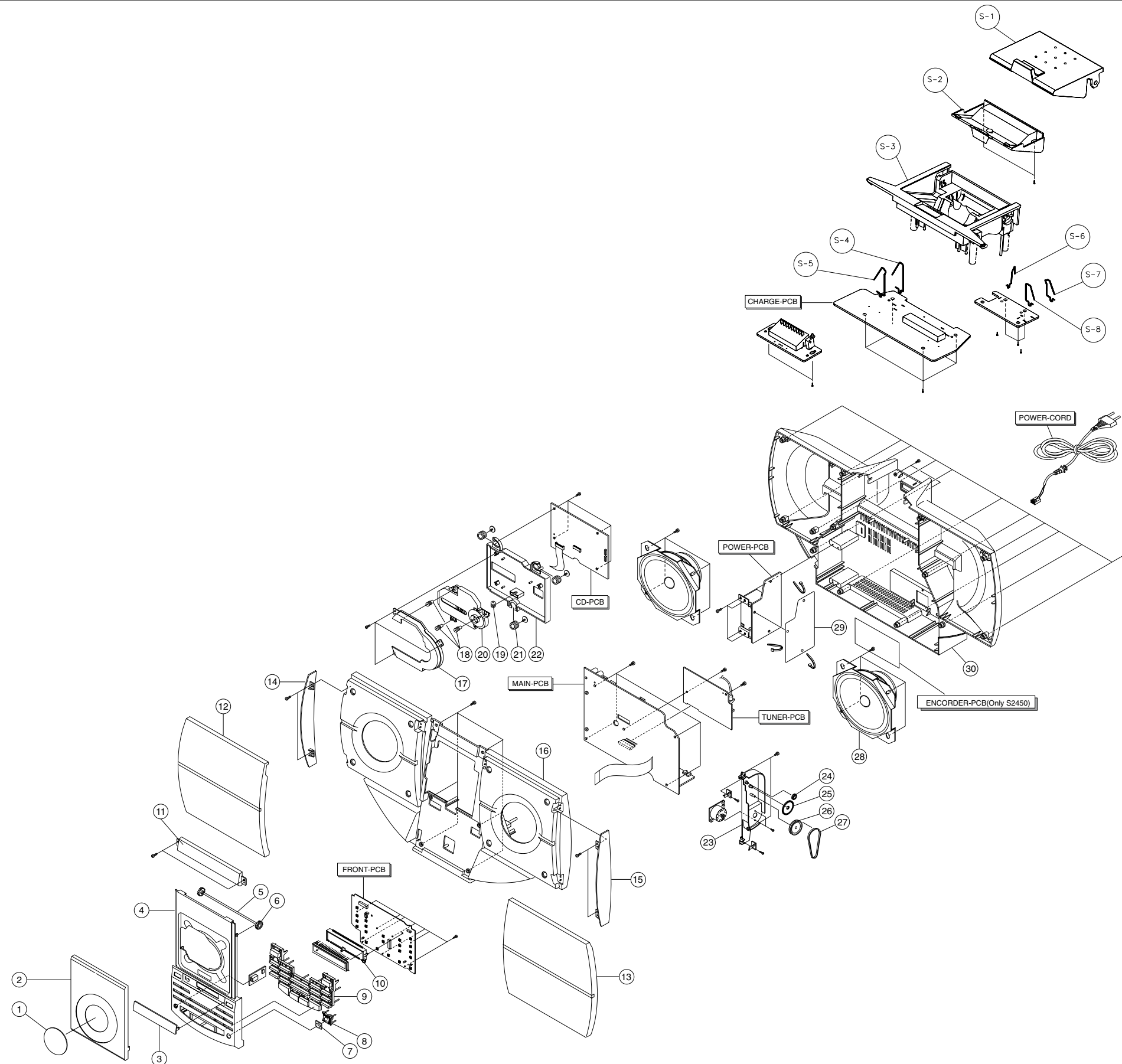


Fig 2-4

### 3.Exploded Views and Parts List



### 3-1 Parts list

No.	Code No.	Description	Specification	Remarks
1	AH64-00318A	DECORATION-CD(EXP);-,ABS,-,L/GRAY,-,S-2000		
2	AH64-00319A	DOOR—CD;-,PMMA,-,SMOKE,-,S-2000		
3	AH64-00321A	WINDOW—VFD;-,PMMA,-,SMOKE,-,S-2000		
4	AH61-00209H	CAP-FRONT;MIPS,-,D/GRAY,-,S-2400/EXP		S-2400
4	AH61-00209J	CAP-FRONT;MIPS,-,D/GRAY,-,S-2450/EXP		S-2450
4	AH61-00209K	CAP-FRONT;MIPS,-,D/GRAY,-,S-2400/EXP		S-2400(RDS)
4	AH61-00209L	CAP-FRONT;MIPS,-,D/GRAY,-,S-2450/EXP		S-2450(RDS)
5	AH61-00220A	SHAFT-GEAR;-,MBS PI4.0,-,142,-,S-2000		
6	AH66-00062A	GEAR-DOOR;ABS,-,S-2000		
7	AH67-00026A	LENS—SOUND;-,PMMA,-,S-2000		
8	AH64-00324A	KNOB—SOUND;-,ABS,-,WHT,-,S-2000		
9	AH64-00322A	KNOB—PLAY;-,ABS,-,L/GRAY,-,S-2000		
10	AH61-00214A	HOLDER—VFD;-,ABS,-,BLK,-,S-2000		
11	AH64-00315A	DECORATION-FRONT;-,MIPS,-,L/GRAY,-,S-2000		
12	AH61-00211A	FRAME-NET,L;-,MIPS,-,BLUE,-,S-2000		
13	AH61-00212A	FRAME-NET,R;-,MIPS,-,BLUE,-,S-2000		
14	AH64-00313A	DECORATION-SIDE L;-,MIPS,-,L/GRAY,-,S-2000		
15	AH64-00314A	DECORATION-SIDE R;-,MIPS,-,L/GRAY,-,S-2000		
16	AH64-00307A	CABINET—FRONT;-,MIPS,-,D/GRAY,-,S-2000		
17	AH61-00210A	CAP-CD;ABS 94HB,-,BLK,-,S-2000		
18	AH73-00007A	RUBBER—MECHA A;BUTYL,-,S-2000,-		
19	AH73-00008A	RUBBER—MECHA B;SILICONE,-,S-2000,-		
20	AH59-00038A	DECK-CDP;DA23.2.0V,BALL,-,S-2000,-		
21	AH73-00010A	RUBBER—HOLDER;BUTYLENE,-,S-2000,-		
22	AH61-00213A	HOLDER—CD;-,ABS,-,GRAY,-,S-2000		
23	AH61-00217A	HOLDER—GEAR;-,ABS,-,BLK,-,S-2000		
24	AH66-00061A	GEAR-S;ABS,-,S-2000		
25	AH66-00060A	GEAR-L;ABS,-,S-2000		
26	AH66-00059A	GEAR-PULLEY;ABS,-,S-2000		
27	AH66-00063A	BELT-PULLEY;-,CR,-,BLK,S-2000		
28	3001-001121	SPEAKER;5W,3.2OHM,82DB,90HZ		
29	AH63-00090A	COVER—PCB;-,PVC 0.5T,-,CLR,-,S-2000		
30	AH64-00308A	CABINET—BACK;-,MIPS,-,D/GRAY,-,S-2000		
S-1	AH64-00717A	DOOR—MP3;-,ABS L/GRAY,-,SILVER,-,S2		
S-2	AH61-00430A	CAP-DOOR;-,ABS,-,GRAY,MP3,S2450		
S-3	AH64-00716A	CABINET TOP;-,ABS,-,D/GRY,-,S2400		
S-4	AH61-00432B	SPRING—BATTERY(+);-,SUS 304 WPB,-,PI 0.6,-,-		
S-5	AH61-00432A	SPRING—BATTERY(-);-,SUS 304 WPB,-,PI 0.6,-,-		
S-6	AH61-00433A	SPRING—BATTERY(G);-,SUS 304 WPB,-,PI 0.6,-,-		
S-7	AH61-00431A	SPRING—BATTERY(+);-,SUS 304 WPB,-,PI 07,-,-		
S-8	AH61-00431B	SPRING-BATTERY(+);SUS304,WPB,PI 0.7,NICO		











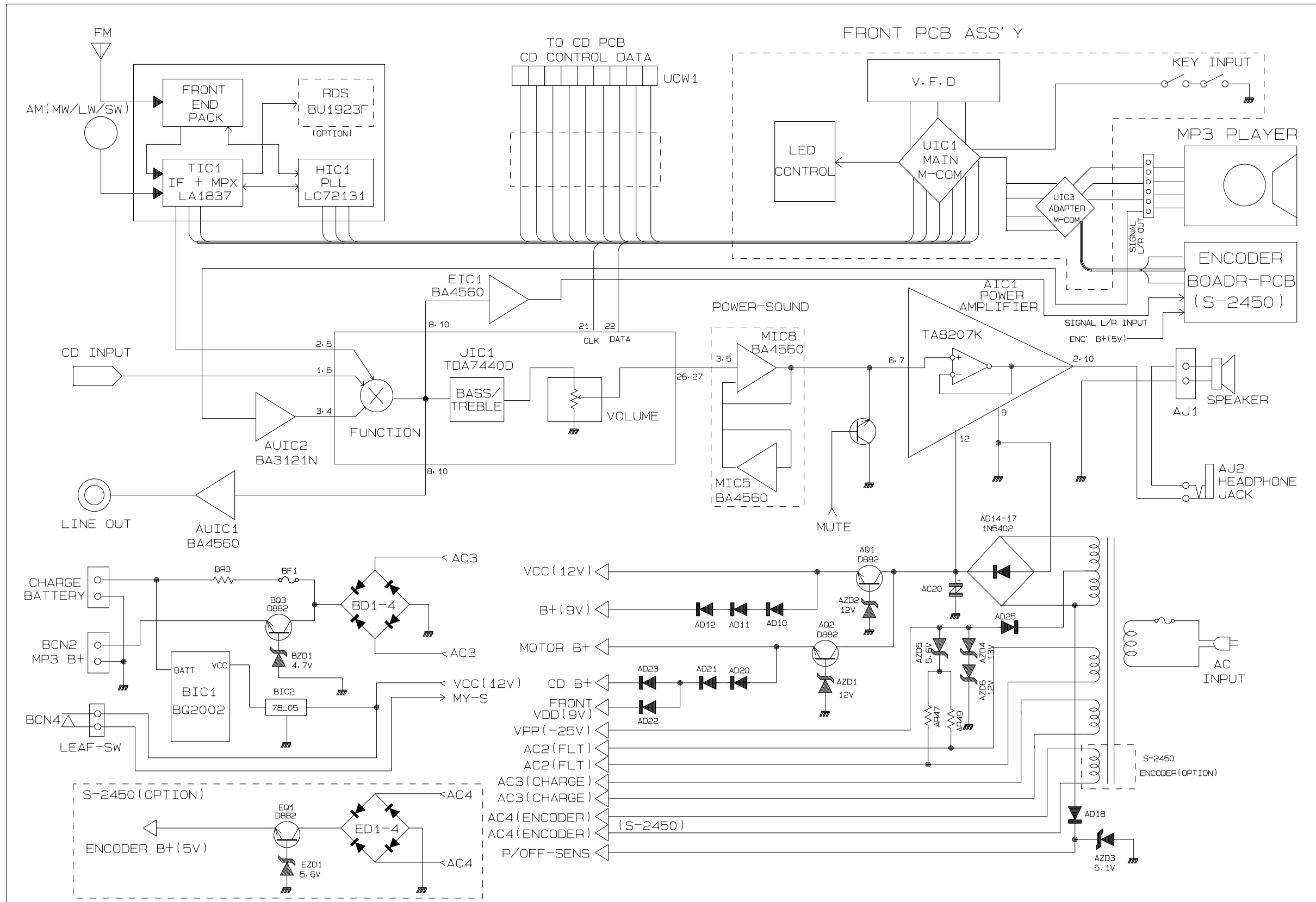


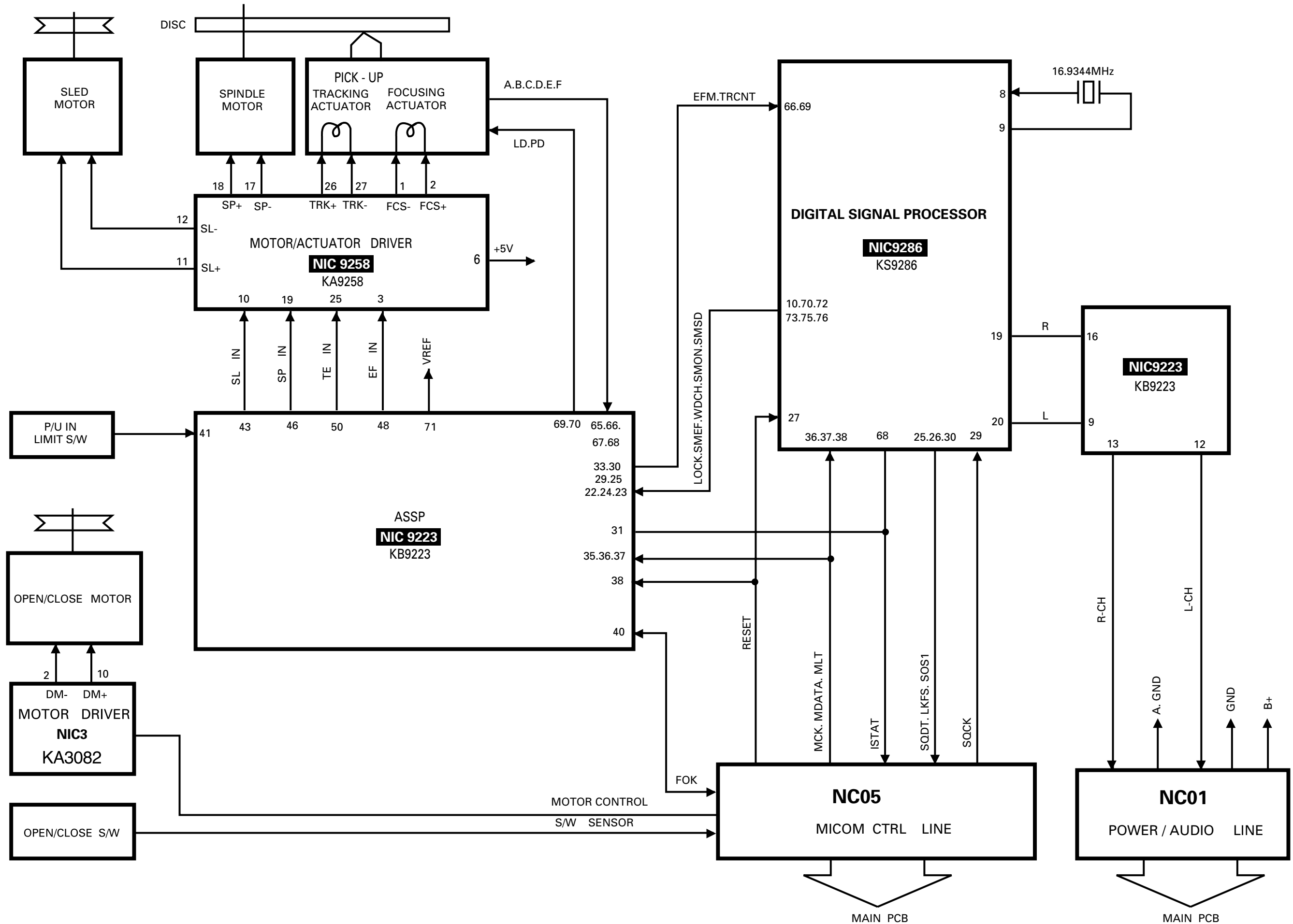


Location no.	Code no.	Description & Specification	Remarks	Location no.	Code no.	Description & Specification	Remarks
NR100	2007-000653	R-CHIP	27Kohm,5%,1/10W,DA,TP,2012	NC2	2202-000797	C-CERAMIC,MLC-AXIAL	10nF,30%,16V,Y5R,3.5x19mm,-,TP
NR113	2007-000653	R-CHIP	27Kohm,5%,1/10W,DA,TP,2012	NC3	2202-000797	C-CERAMIC,MLC-AXIAL	10nF,30%,16V,Y5R,3.5x19mm,-,TP
NR119	2007-000653	R-CHIP	27Kohm,5%,1/10W,DA,TP,2012	NC4	2301-000476	C-FILM,PEF	82nF,10%,50V,10.5x12.5x6,TP
NR101	2007-000686	R-CHIP	3.3Kohm,5%,1/10W,DA,TP,2012	NC21	2401-000240	C-AL	100uF,20%,10V,GP,5*11mm,5mm,TP
NR103	2007-000686	R-CHIP	3.3Kohm,5%,1/10W,DA,TP,2012	NC10	2401-000244	C-AL	100uF,20%,10V,GP,6.3*7,5MM,TP
NR140	2007-000774	R-CHIP	33Kohm,5%,1/10W,DA,TP,2012	NC12	2401-000244	C-AL	100uF,20%,10V,GP,6.3*7,5MM,TP
NR138	2007-000778	R-CHIP	330Kohm,5%,1/10W,DA,TP,2012	NC15	2401-000244	C-AL	100uF,20%,10V,GP,6.3*7,5MM,TP
NR109	2007-000872	R-CHIP	4.7Kohm,5%,1/10W,DA,TP,2012	NC22	2401-000244	C-AL	100uF,20%,10V,GP,6.3*7,5MM,TP
NR111	2007-000872	R-CHIP	4.7Kohm,5%,1/10W,DA,TP,2012	NC11	2401-000407	C-AL	10uF,20%,16V,GP,3.5*5.2,5MM,TP
NR115	2007-000872	R-CHIP	4.7Kohm,5%,1/10W,DA,TP,2012	NC13	2401-000778	C-AL	220uF,20%,10V,GP,6.3x11.5MM,TP
NR117	2007-000872	R-CHIP	4.7Kohm,5%,1/10W,DA,TP,2012	NC14	2401-000778	C-AL	220uF,20%,10V,GP,6.3x11.5MM,TP
NR125	2007-000872	R-CHIP	4.7Kohm,5%,1/10W,DA,TP,2012	NC16	2401-001102	C-AL	330uF,20%,16V,GP,8*11.5,5MM,TP
NR114	2007-000898	R-CHIP	430Kohm,5%,1/10W,DA,TP,2012	NC17	2401-001893	C-AL	100uF,20%,16V,GP,6*7,5MM,TP
NR137	2007-000941	R-CHIP	47Kohm,5%,1/10W,DA,TP,2012	NC18	2401-001952	C-AL	4.7uF,20%,50V,GP,5*7,5mm,TP
NR141	2007-000941	R-CHIP	47Kohm,5%,1/10W,DA,TP,2012	NC19	2401-001952	C-AL	4.7uF,20%,50V,GP,5*7,5mm,TP
NR142	2007-000941	R-CHIP	47Kohm,5%,1/10W,DA,TP,2012	NC20	2401-001952	C-AL	4.7uF,20%,50V,GP,5*7,5mm,TP
NR144	2007-000941	R-CHIP	47Kohm,5%,1/10W,DA,TP,2012	NC23	2401-001952	C-AL	4.7uF,20%,50V,GP,5*7,5mm,TP
NR129	2007-001039	R-CHIP	56Kohm,5%,1/10W,DA,TP,2012	NC24	2401-001952	C-AL	4.7uF,20%,50V,GP,5*7,5mm,TP
NR130	2007-001039	R-CHIP	56Kohm,5%,1/10W,DA,TP,2012	NC25	2401-001952	C-AL	4.7uF,20%,50V,GP,5*7,5mm,TP
NR131	2007-001039	R-CHIP	56Kohm,5%,1/10W,DA,TP,2012	NL2	2701-000114	INDUCTOR-AXIAL	10uH,10%,2.5x3.4mm,LA02TB100K
NR132	2007-001039	R-CHIP	56Kohm,5%,1/10W,DA,TP,2012	NL3	2701-000114	INDUCTOR-AXIAL	10uH,10%,2.5x3.4mm,LA02TB100K
NR149	2007-001177	R-CHIP	8.2Kohm,5%,1/10W,DA,TP,2012	NL4	2701-000114	INDUCTOR-AXIAL	10uH,10%,2.5x3.4mm,LA02TB100K
NR120	2007-001216	R-CHIP	82ohm,5%,1/10W,DA,TP,2012	NL1	2701-000178	INDUCTOR-AXIAL	33uH,10%,2.8x7mm,BAL03ST330K
NC100	2203-000206	C-CERAMIC,CHIP	100nF,10%,50V,X7R,2012,-,TP	NJ11	3301-000297	CORE-FERRITE BEAD	AA,3.6x1.2x5.7mm,1400,2900G
NC106	2203-000206	C-CERAMIC,CHIP	100nF,10%,50V,X7R,2012,-,TP	NJ20	3301-000297	CORE-FERRITE BEAD	AA,3.6x1.2x5.7mm,1400,2900G
NC108	2203-000206	C-CERAMIC,CHIP	100nF,10%,50V,X7R,2012,-,TP	NJ1-8	3811-000389	WIRE-NO SHEATH CU	SPCW,300V,52.4mm,1/0.5mm,-,-
NC109	2203-000206	C-CERAMIC,CHIP	100nF,10%,50V,X7R,2012,-,TP	NJ21-39	3811-000389	WIRE-NO SHEATH CU	SPCW,300V,52.4mm,1/0.5mm,-,-
NC117	2203-000206	C-CERAMIC,CHIP	100nF,10%,50V,X7R,2012,-,TP	3809-001146	3809-001146	CABLE-FLAT	30V,80C,60MM,16P,1.25MM,UL2886
NC122	2203-000206	C-CERAMIC,CHIP	100nF,10%,50V,X7R,2012,-,TP	3001-001121	3001-001121	SPEAKER	5W,3.2OHM,82DB,90HZ
NC127	2203-000206	C-CERAMIC,CHIP	100nF,10%,50V,X7R,2012,-,TP				
NC129	2203-000206	C-CERAMIC,CHIP	100nF,10%,50V,X7R,2012,-,TP				
NC131	2203-000206	C-CERAMIC,CHIP	100nF,10%,50V,X7R,2012,-,TP				
NC133	2203-000206	C-CERAMIC,CHIP	100nF,10%,50V,X7R,2012,-,TP				
NC134	2203-000206	C-CERAMIC,CHIP	100nF,10%,50V,X7R,2012,-,TP				
NC135	2203-000206	C-CERAMIC,CHIP	100nF,10%,50V,X7R,2012,-,TP				
NC147	2203-000206	C-CERAMIC,CHIP	100nF,10%,50V,X7R,2012,-,TP				
NC111	2203-000260	C-CERAMIC,CHIP	10nF,10%,50V,X7R,2012,-,TP				
NC114	2203-000260	C-CERAMIC,CHIP	10nF,10%,50V,X7R,2012,-,TP				
NC115	2203-000260	C-CERAMIC,CHIP	10nF,10%,50V,X7R,2012,-,TP				
NC116	2203-000260	C-CERAMIC,CHIP	10nF,10%,50V,X7R,2012,-,TP				
NC119	2203-000260	C-CERAMIC,CHIP	10nF,10%,50V,X7R,2012,-,TP				
NC132	2203-000260	C-CERAMIC,CHIP	10nF,10%,50V,X7R,2012,-,TP				
NC141	2203-000260	C-CERAMIC,CHIP	10nF,10%,50V,X7R,2012,-,TP				
NC144	2203-000260	C-CERAMIC,CHIP	10nF,10%,50V,X7R,2012,-,TP				
NC146	2203-000260	C-CERAMIC,CHIP	10nF,10%,50V,X7R,2012,-,TP				
NC147	2203-000260	C-CERAMIC,CHIP	10nF,10%,50V,X7R,2012,-,TP				
NC148	2203-000260	C-CERAMIC,CHIP	10nF,10%,50V,X7R,2012,-,TP				
NC149	2203-000260	C-CERAMIC,CHIP	10nF,10%,50V,X7R,2012,-,TP				
NC123	2203-000477	C-CERAMIC,CHIP	1uF,+80-20%,16V,Y5V,2012,-,TP				
NC138	2203-000495	C-CERAMIC,CHIP	2.2nF,10%,50V,X7R,2012,-,TP				
NC120	2203-000648	C-CERAMIC,CHIP	24pF,5%,50V,NPO,2012,-,TP				
NC140	2203-000787	C-CERAMIC,CHIP	330PF,0.1,50V,Y5P,2012,-,TP				
NC110	2203-000802	C-CERAMIC,CHIP	33nF,10%,50V,X7R,2012,TP				
NC118	2203-000802	C-CERAMIC,CHIP	33nF,10%,50V,X7R,2012,TP				
NC121	2203-000892	C-CERAMIC,CHIP	4.7nF,10%,50V,X7R,TP,2012				
NC124	2203-000892	C-CERAMIC,CHIP	4.7nF,10%,50V,X7R,TP,2012				
NC125	2203-000892	C-CERAMIC,CHIP	4.7nF,10%,50V,X7R,TP,2012				
NC139	2203-000953	C-CERAMIC,CHIP	470pF,5%,50V,NPO,2012,-,TP				
NC102	2203-000979	C-CERAMIC,CHIP	47nF,10%,50V,X7R,2012,-,TP				
NC143	2203-000979	C-CERAMIC,CHIP	47nF,10%,50V,X7R,2012,-,TP				
NC142	2203-001137	C-CERAMIC,CHIP	68nF,+80-20%,50V,Y5V,2012,-,TP				
NC112	2203-001537	C-CERAMIC,CHIP	1nF,10%,50V,X7R,2012				
NC113	2203-001537	C-CERAMIC,CHIP	1nF,10%,50V,X7R,2012				
NC128	2203-001537	C-CERAMIC,CHIP	1nF,10%,50V,X7R,2012				
NC136	2203-001537	C-CERAMIC,CHIP	1nF,10%,50V,X7R,2012				
NC137	2203-001537	C-CERAMIC,CHIP	1nF,10%,50V,X7R,2012				
NC103	2203-001551	C-CERAMIC,CHIP	1.5nF,10%,50V,X7R,2012,-,TP				
NC122	2203-001551	C-CERAMIC,CHIP	1.5nF,10%,50V,X7R,2012,-,TP				
NC101	2203-001619	C-CERAMIC,CHIP	27pF,5%,50V,NPO,2012,-,TP				
NC104	2203-001619	C-CERAMIC,CHIP	27pF,5%,50V,NPO,2012,-,TP				
NC105	2203-001619	C-CERAMIC,CHIP	27pF,5%,50V,NPO,2012,-,TP				
NC88	2401-000795	C-AL	220uF,20%,16V,GP,8*12,5MM,TP				
NC89	2401-001965	C-AL	470nF,20%,50V,BK,4*7,5MM,TP				
NC90	2401-001965	C-AL	470nF,20%,50V,BK,4*7,5MM,TP				
NC01	AH39-00103A	LEAD CONNECTOR-ASS	-,5295,51004,8P,210MM,1007#				
NC02	AH39-00111A	LEAD CONNECTOR-ASSY	-,5295,51004,5P,70MM,1007#				
NC3	1003-001162	IC-MOTOR DRIVER	KA3082,SIP,10PIN,25MIL,D				
NXF1	2802-000211	RESONATOR-CERAMIC	16.93MHZ,0.5%,TP,10.0x5.0x10.0				
NC05	3708-000413	CONNECTOR-FPC/FC/PIC	16P,1.25mm,ANGLE,				
NCW100	3708-001388	CONNECTOR-FPC/FC/PIC	16P,1MM,ANGLE,SN				
NC03	3711-004381	CONNECTOR-HEADER	BOX,6P,1R,1.5MM,STRAIGHT,SN				
NC1	AC14-12001G	IC-POSI,ADJUST REG.	KA78L05Z-TA,TO-92,3P,-,PLASTIC				
NC2	AC14-12001G	IC-POSI,ADJUST REG.	KA78L05Z-TA,TO-92,3P,-,PLASTIC				
ND1-4	0401-000101	DIODE-SWITCHING	1N4148,100V,200mA,500mW,4nS				
NZD1	0403-000361	DIODE-ZENER	UZ6.2BSB,6.2V,5.99-6.24V,50				
NTR1	0501-000610	TR-SMALL SIGNAL	KSA928A,PNP,-30V,-30V,-2A,1W				
NR12	2001-000319	R-CARBON	120Kohm,5%,1/8W,AA,TP,1.8x3.2mm				
NJ9	2001-000429	R-CARBON	1Kohm,5%,1/8W,AA,TP,1.8x3.2mm				
NR13	2001-000429	R-CARBON	1Kohm,5%,1/8W,AA,TP,1.8x3.2mm				
NR3-10	2001-000429	R-CARBON	1Kohm,5%,1/8W,AA,TP,1.8x3.2mm				
NR14	2001-000527	R-CARBON	22ohm,5%,1/8W,AA,TP,1.8x3.2mm				
NFR1	2008-000140	R-FUSIBLE	2.2ohm,5%,1/2W,AA,TP,3.5x9.4mm				
NC1	2202-000797	C-CERAMIC,MLC-AXIAL	10nF,30%,16V,Y5R,3.5x19mm,-,TP				

# 5. Block Diagram

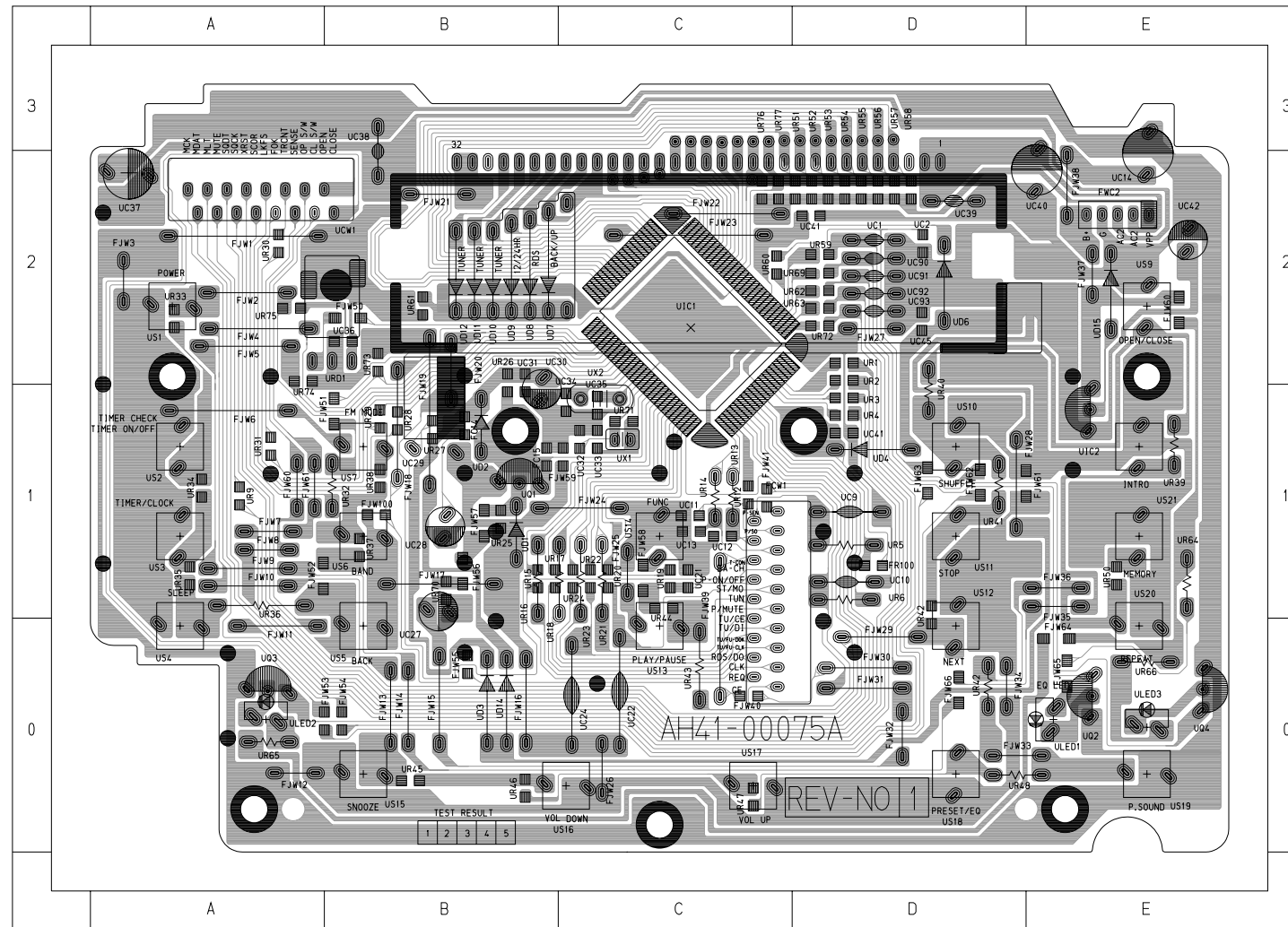
## 5-1 Main



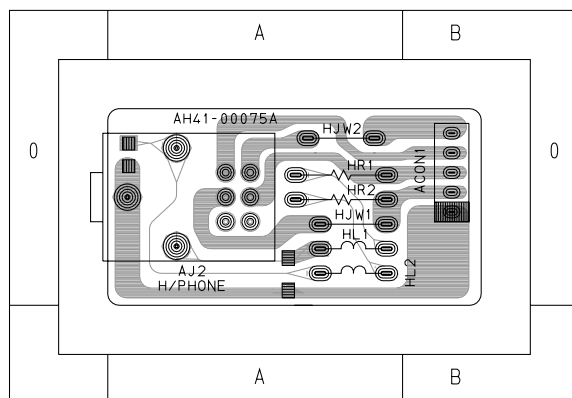


## 6. Printed Circuit Board Diagram

### 6-1 FRONT



### 6-2 Headphone PCB



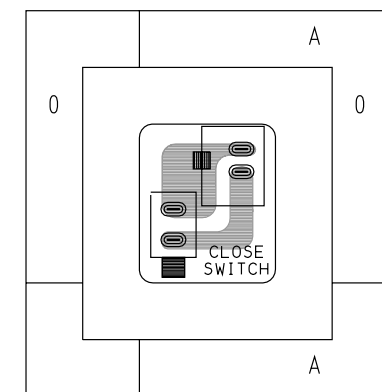
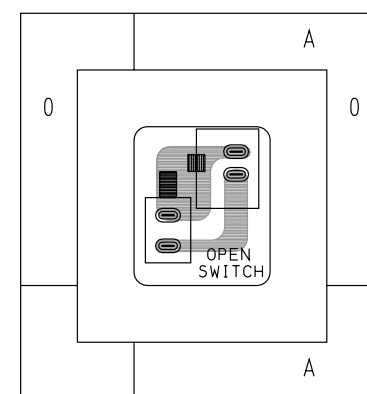
\*\*\* RESISTOR \*\*\*

HR1 (A0)  
HR2 (A0)

\*\*\* IC&WAFER \*\*\*

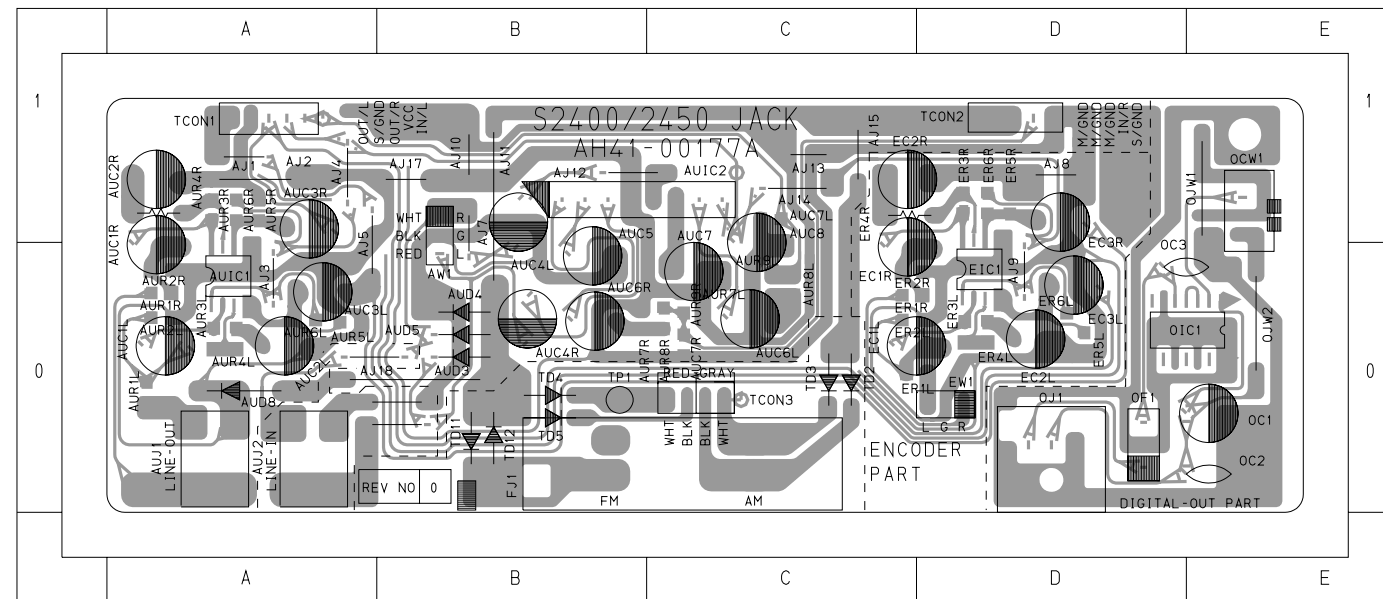
ACON1 (B0)

### 6-3 Open/Close PCB





### 6-4 Jack PCB



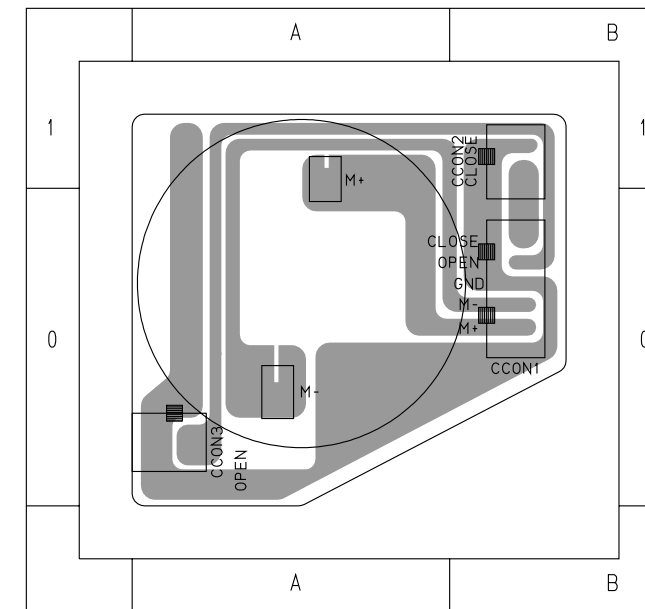
\*\*\* DIODE \*\*\*

- AUD3 (B0)
- AUD4 (B0)
- AUD5 (B0)
- AUD8 (A0)
- TD11 (B0)
- TD12 (B0)
- TD2 (C0)
- TD3 (C0)
- TD4 (B0)
- TD5 (B0)

\*\*\* IC&WAFER \*\*\*

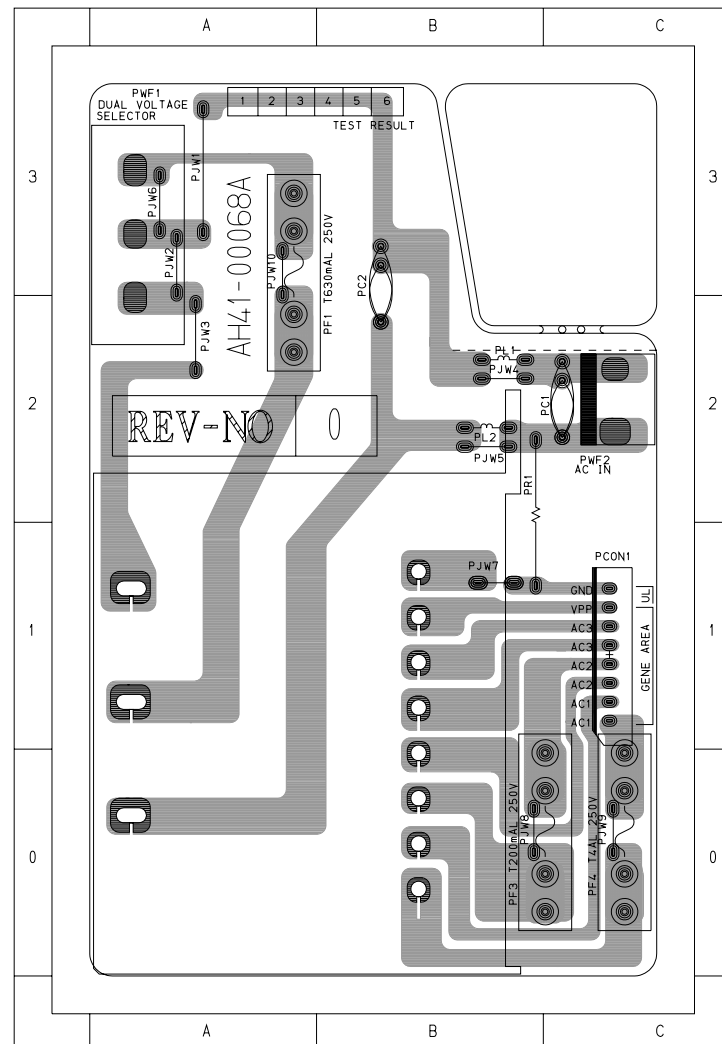
- AUIC2 (B1)
- AW1 (B1)
- EW1 (D0)
- OCW1 (E1)
- OF1 (D0)
- TCON3 (C0)

### 6-5 Motor PCB



\*\*\* IC&WAFER \*\*\*

### 6-6 POWER(S-2400/2450)



\*\*\* RESISTOR \*\*\*

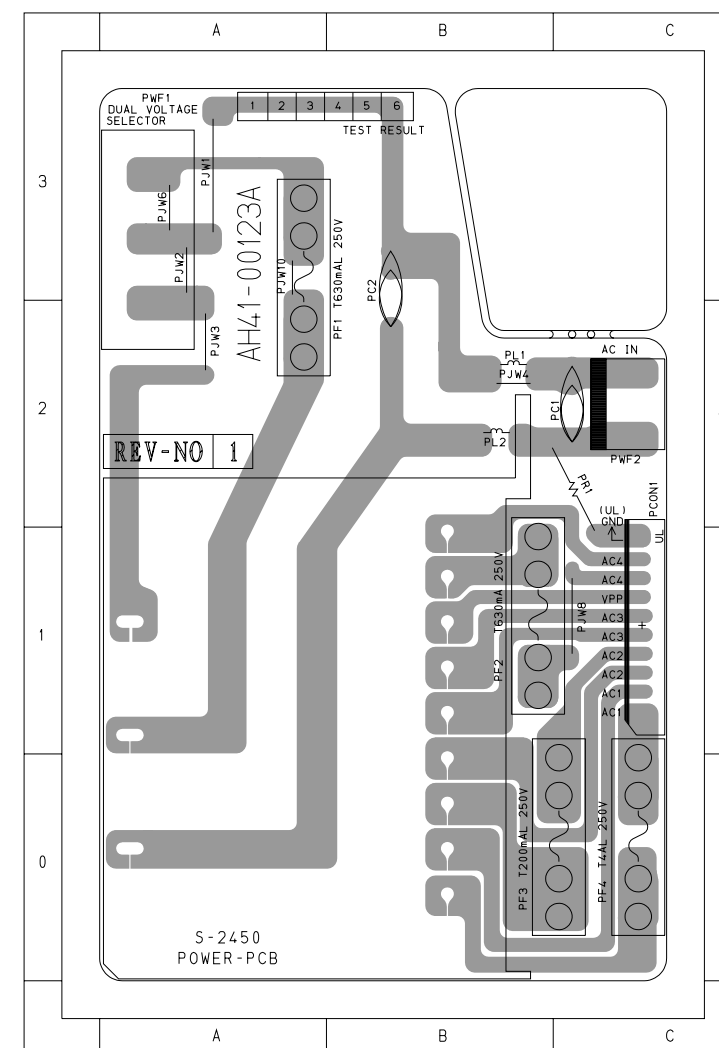
- PR1 (B1)

\*\*\* CONDENSER \*\*\*

- PL1 (B2)
- PL2 (B2)

\*\*\* IC&WAFER \*\*\*

- PCON1 (C1)



\*\*\* RESISTOR \*\*\*

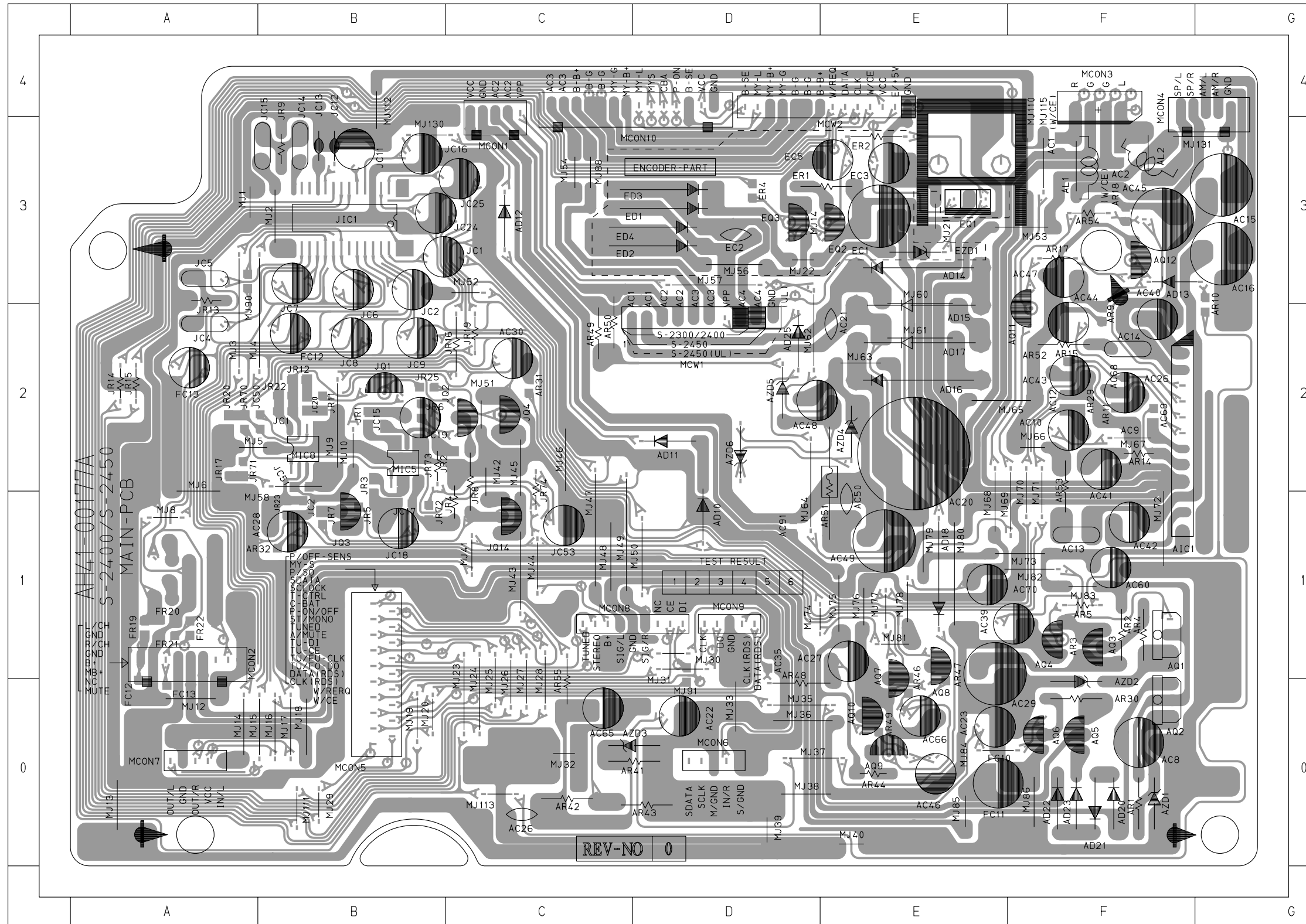
- PR1 (B2)

\*\*\* CONDENSER \*\*\*

- PL1 (B2)
- PL2 (B2)

\*\*\* IC&WAFER \*\*\*

- PCON1 (C1)



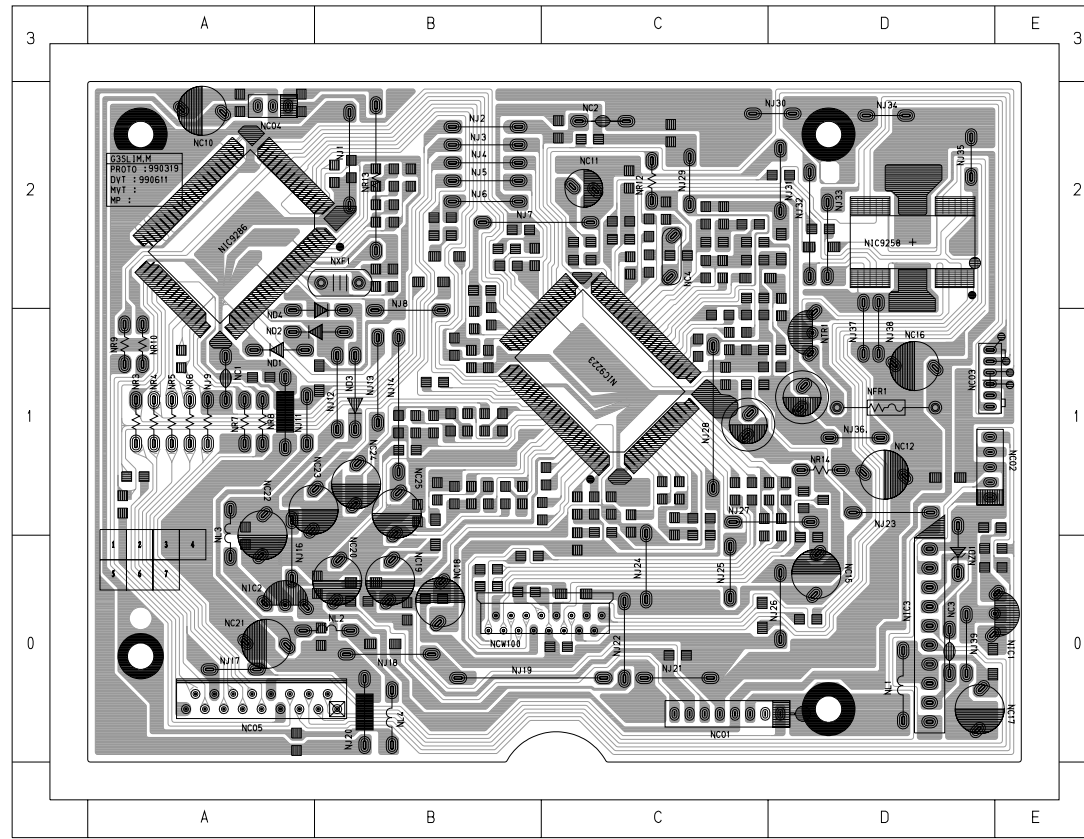
\*\*\* DIODE \*\*\*    \*\*\* IC&WAFER \*\*\*

- AD10 (D2)    \* (B2)
- AD11 (D2)    \* (B2)
- AD12 (C3)    MCON1 (C4)
- AD13 (F3)    MCON10 (D4)
- AD18 (E1)    MCON2 (A1)
- AD20 (F0)    MCON3 (F4)
- AD21 (F0)    MCON4 (G4)
- AD22 (F0)    MCW1 (D2)
- AD23 (F0)    MCW2 (E4)
- AD25 (D2)
- AZD1 (F0)
- AZD2 (F0)
- AZD3 (C0)
- AZD4 (E2)
- AZD5 (D2)
- AZD6 (D2)
- ED1 (D3)
- ED2 (D3)
- ED3 (D3)
- ED4 (D3)
- EZD1 (E3)

\*\*\* TR \*\*\*

- AQ10 (E0)
- AQ11 (F2)
- AQ12 (F3)
- AQ3 (F1)
- AQ4 (F1)
- AQ5 (F0)
- AQ6 (F0)
- AQ7 (E1)
- AQ8 (E1)
- AQ9 (E0)
- EQ2 (E3)
- EQ3 (D3)
- JQ1 (B2)
- JQ2 (C2)
- JQ3 (B1)
- JQ4 (C2)

6-8 CD



\*\*\* CONDENSER \*\*\*    \*\*\* DIODE \*\*\*

- NC1 (A1)
- NC10 (A2)
- NC11 (C2)
- NC12 (D1)
- NC15 (D0)
- NC16 (D1)
- NC17 (D0)
- NC18 (B0)
- NC19 (B0)
- NC2 (C2)
- NC20 (B0)
- NC21 (A0)
- NC22 (A1)
- NC23 (A1)
- NC24 (B1)
- NC25 (B1)
- NC3 (D0)
- NC4 (C2)
- ND1 (A1)
- ND2 (A1)
- ND3 (B1)
- ND4 (B1)
- NZD1 (D0)

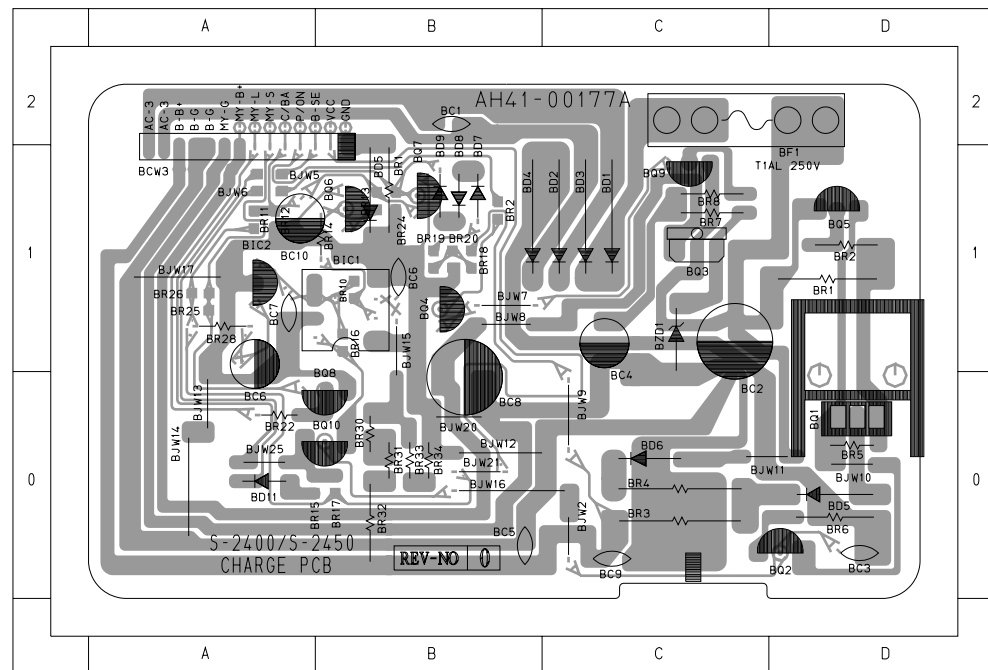
\*\*\* IC&WAFER \*\*\*

- NC01 (C0)
- NC02 (D1)
- NC04 (A2)
- NC05 (A0)
- NIC3 (D0)

\*\*\* TR \*\*\*

- NIC1 (E0)
- NIC2 (A0)
- NTR1 (D1)

6-9 MP3 Charge/MP3 Jack

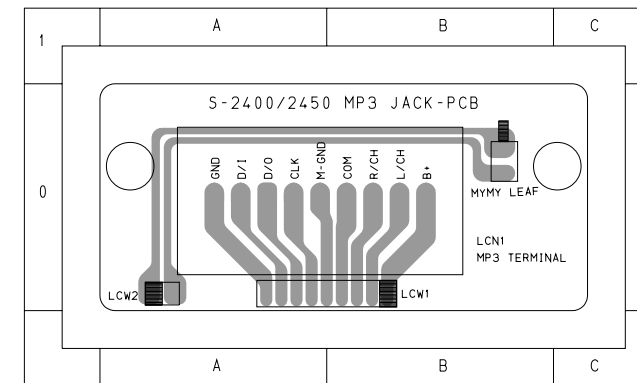


\*\*\* DIODE \*\*\*    \*\*\* IC&WAFER \*\*\*

- BD1 (C1)
- BD11 (A0)
- BD2 (C1)
- BD3 (C1)
- BD4 (B1)
- BD5 (D0)
- BD6 (B1)
- BD7 (B1)
- BD8 (B1)
- BD9 (B1)
- BZD1 (C1)
- BCW3 (A1)
- BIC1 (B1)

\*\*\* TR \*\*\*

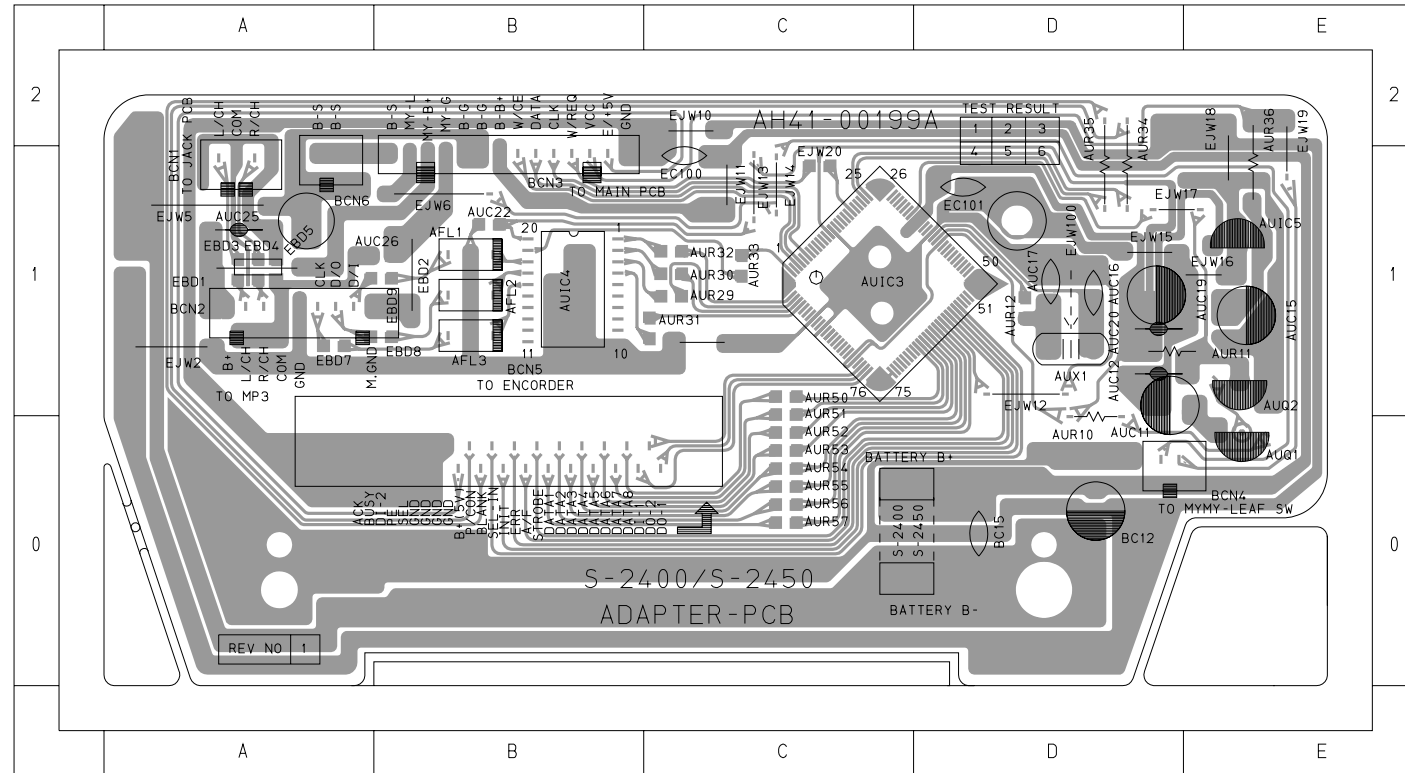
- BIC2 (A1)
- BQ10 (B0)
- BQ2 (C0)
- BQ4 (B1)
- BQ5 (D1)
- BQ6 (B1)
- BQ7 (B1)
- BQ8 (B0)
- BQ9 (C1)



\*\*\* IC&WAFER \*\*\*

- LCW1 (B0)
- LCW2 (A0)

## 6-10 Adapter/Batt-Sense

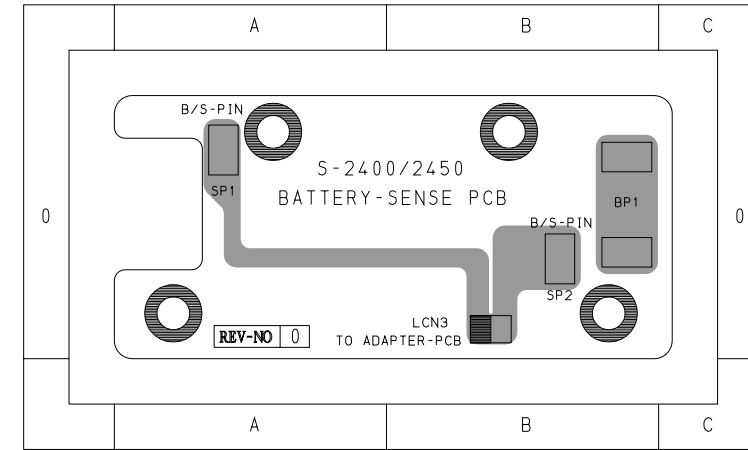


\*\*\* TR \*\*\*

AUC5 (E1)  
 AUQ1 (E0)  
 AUQ2 (E1)

\*\*\* IC&WAFER \*\*\*

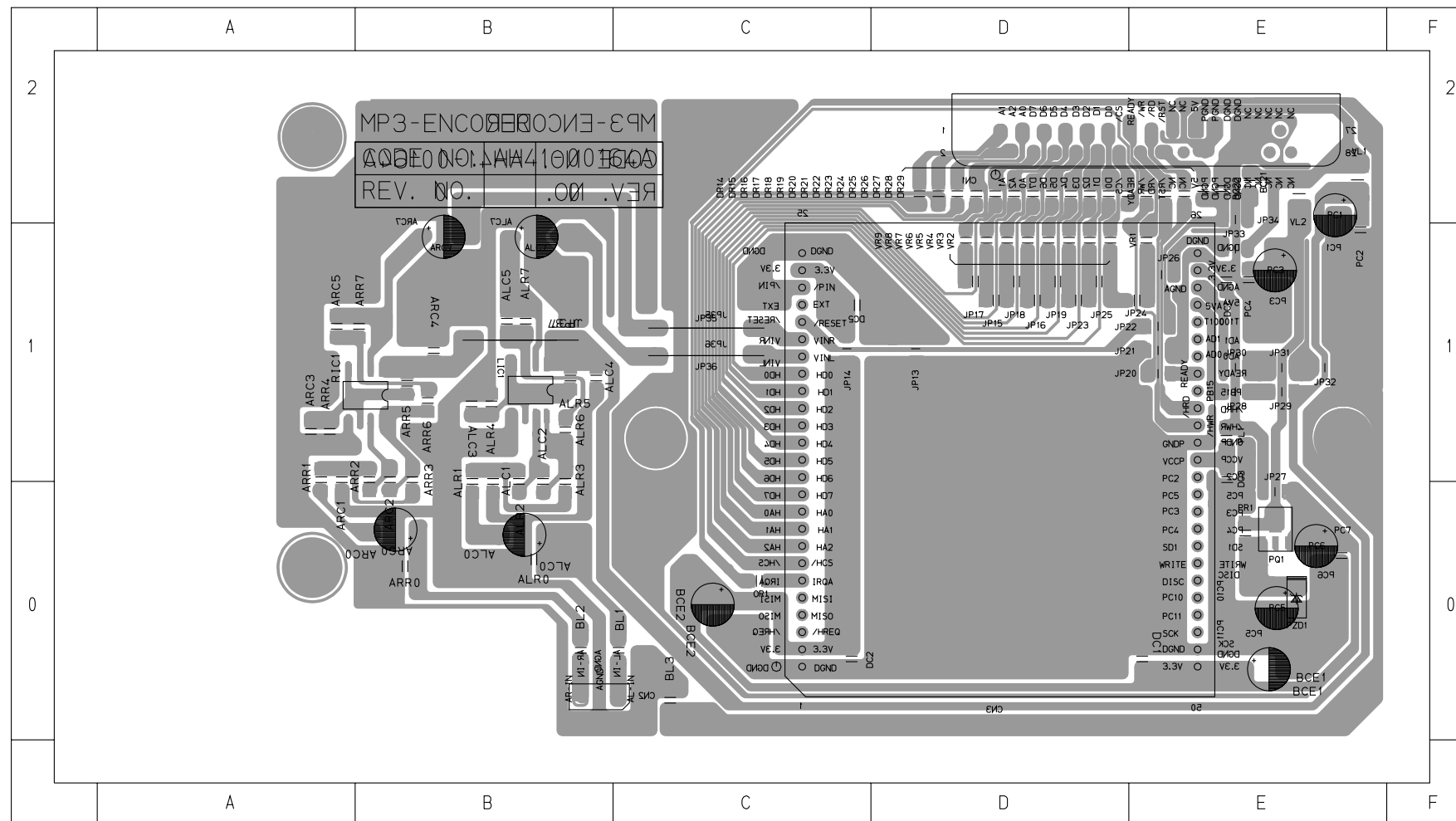
BCN1 (A1)  
 BCN2 (A1)  
 BCN3 (B1)  
 BCN4 (D0)  
 BCN6 (A1)



\*\*\* IC&WAFER \*\*\*

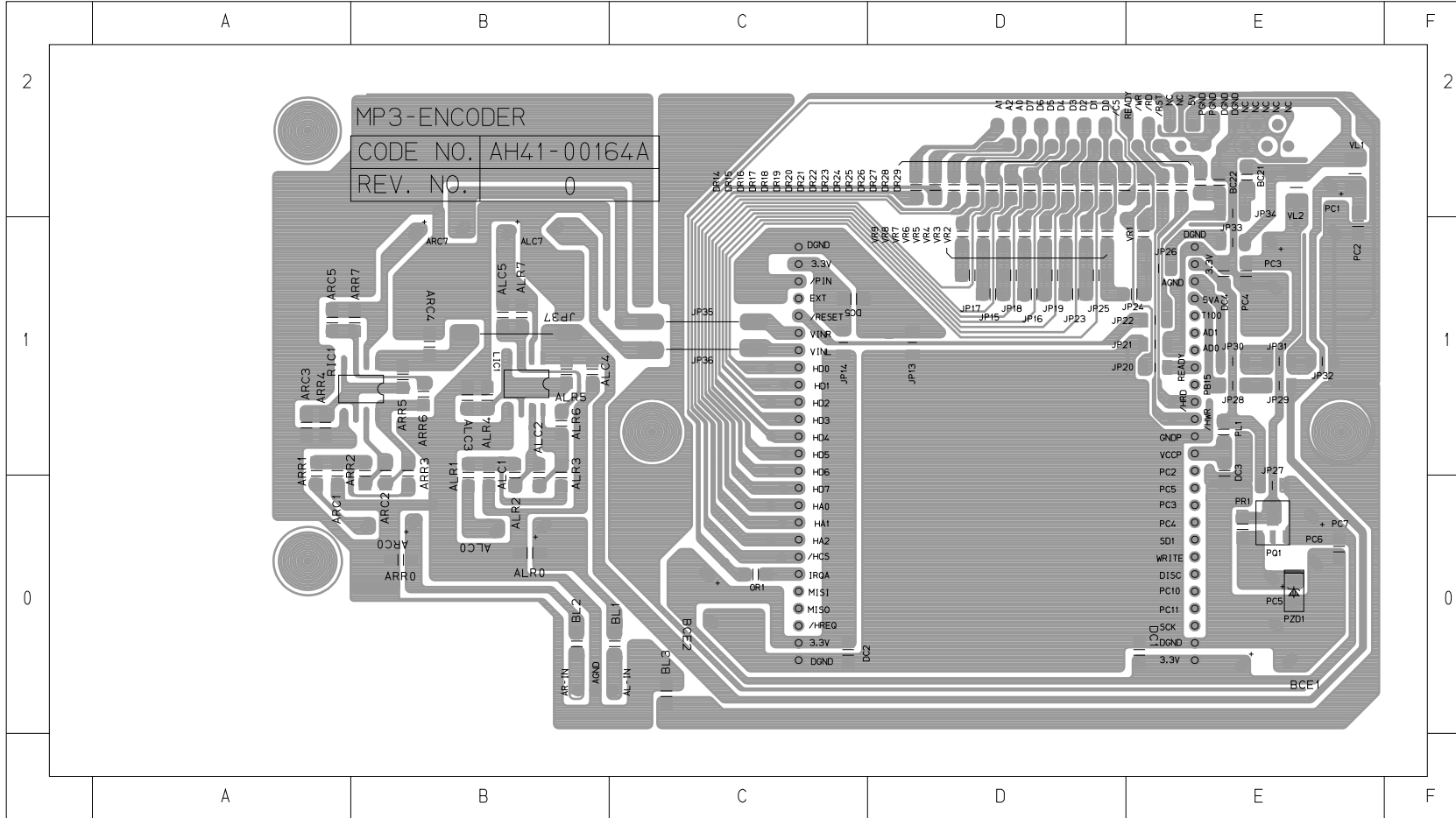
LCN3 (B0)

## 6-11 ENCODER



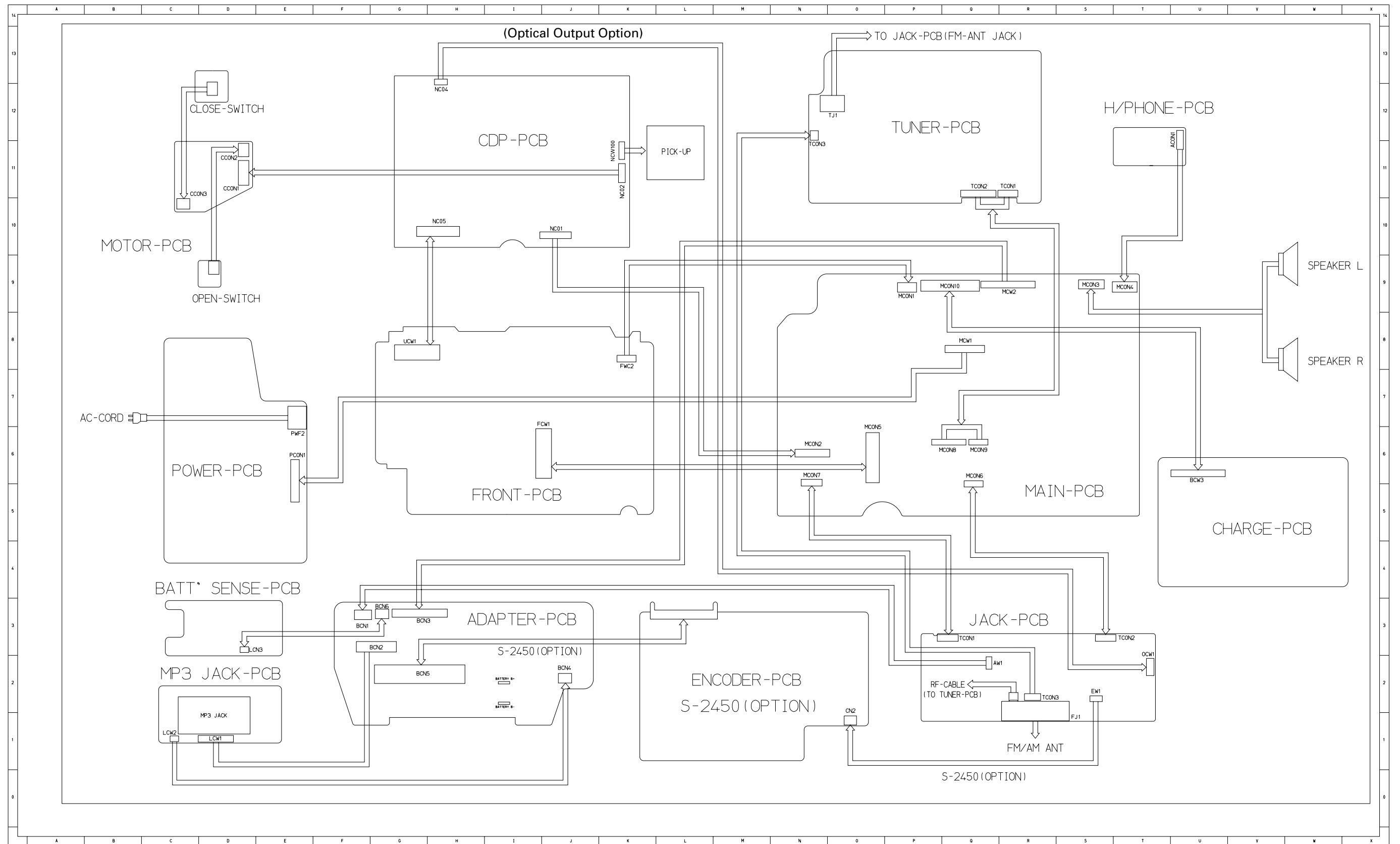


# ENCODER PCB DIAGRAM (only S2450 Model)



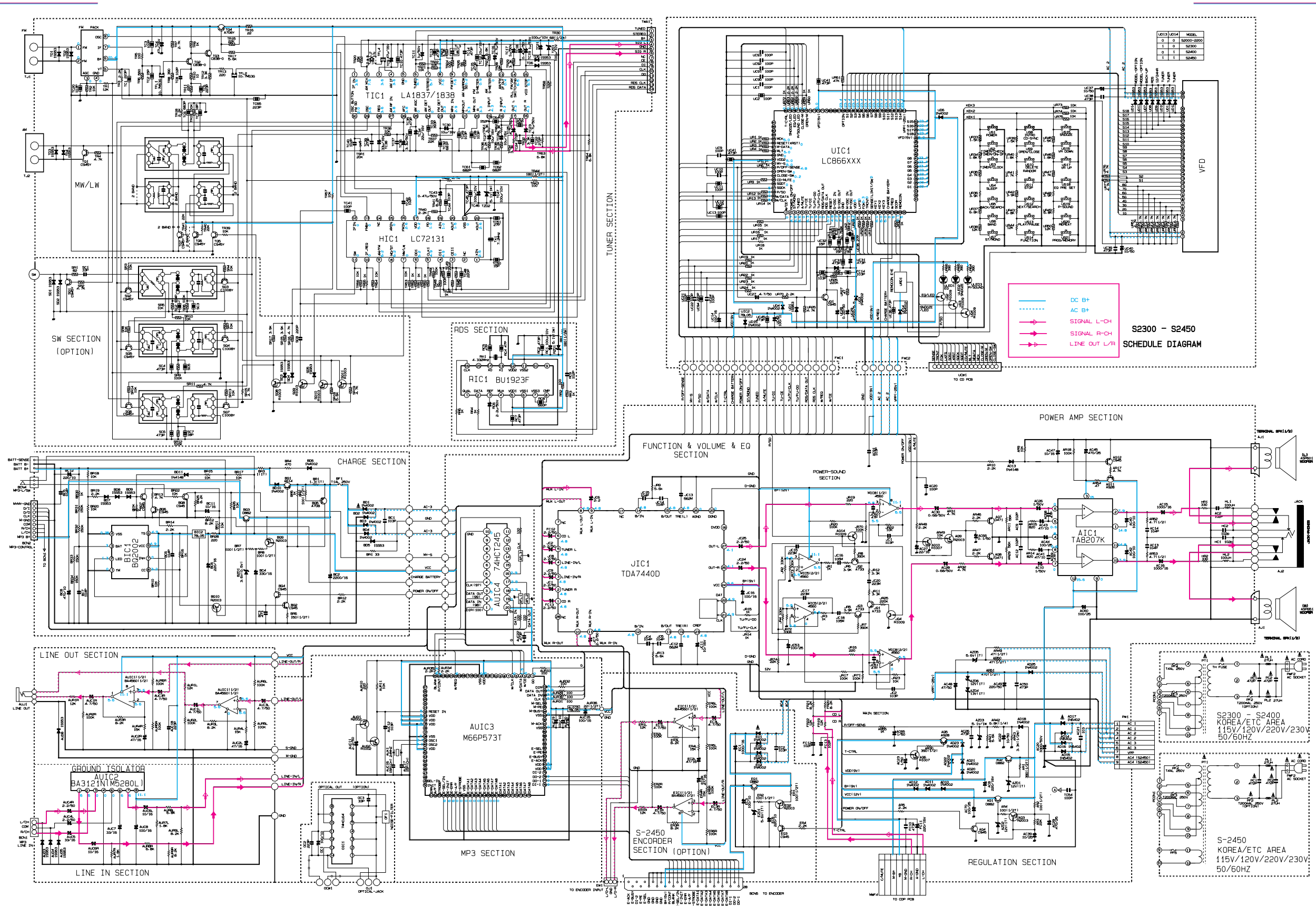
MP3-ENCODER  
 CODE NO. AH41-00164A  
 REV. NO. 0

## 7. Wiring Diagram

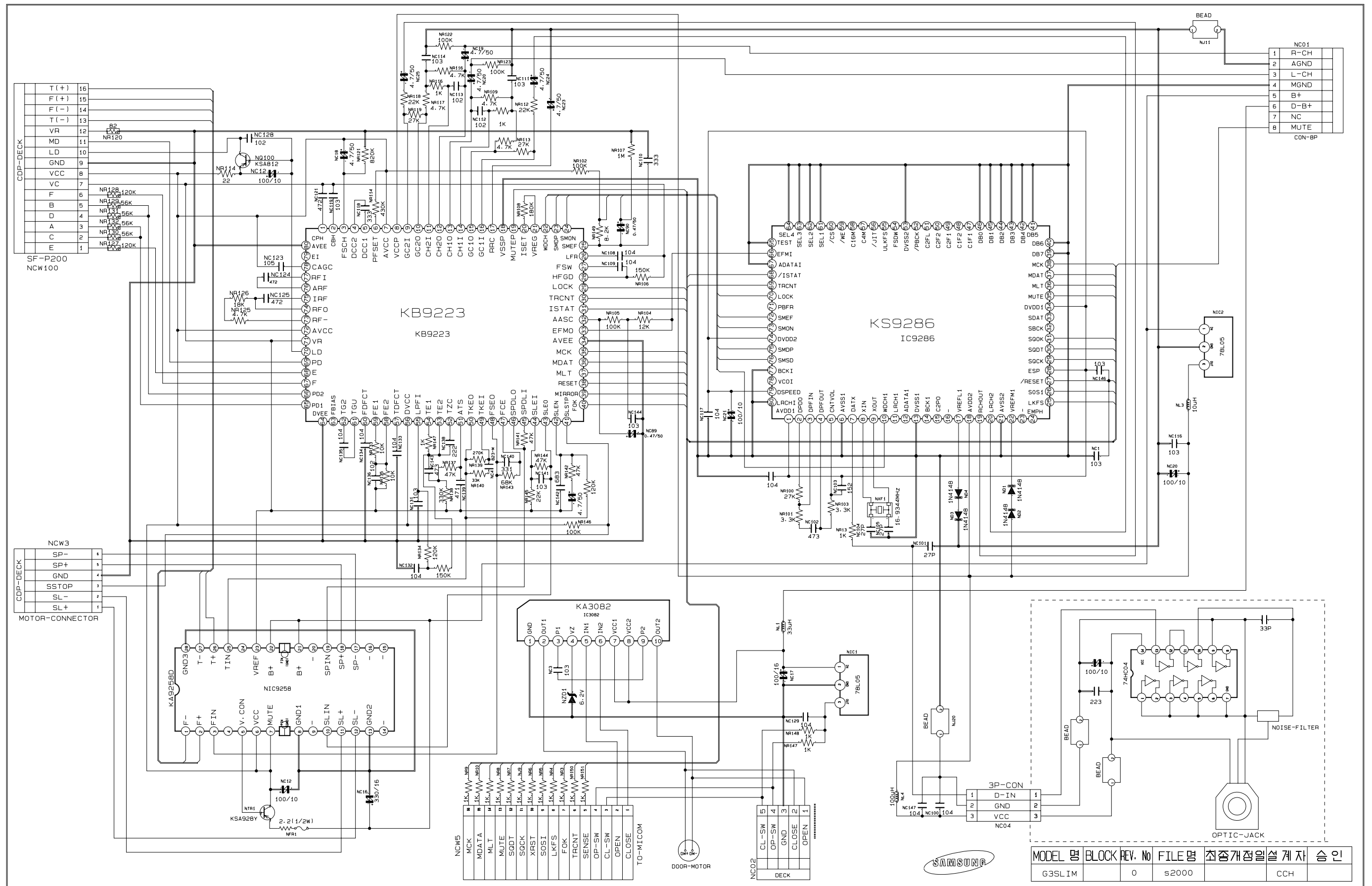


# 8. Schematic Diagram

## 8-1 Main Part

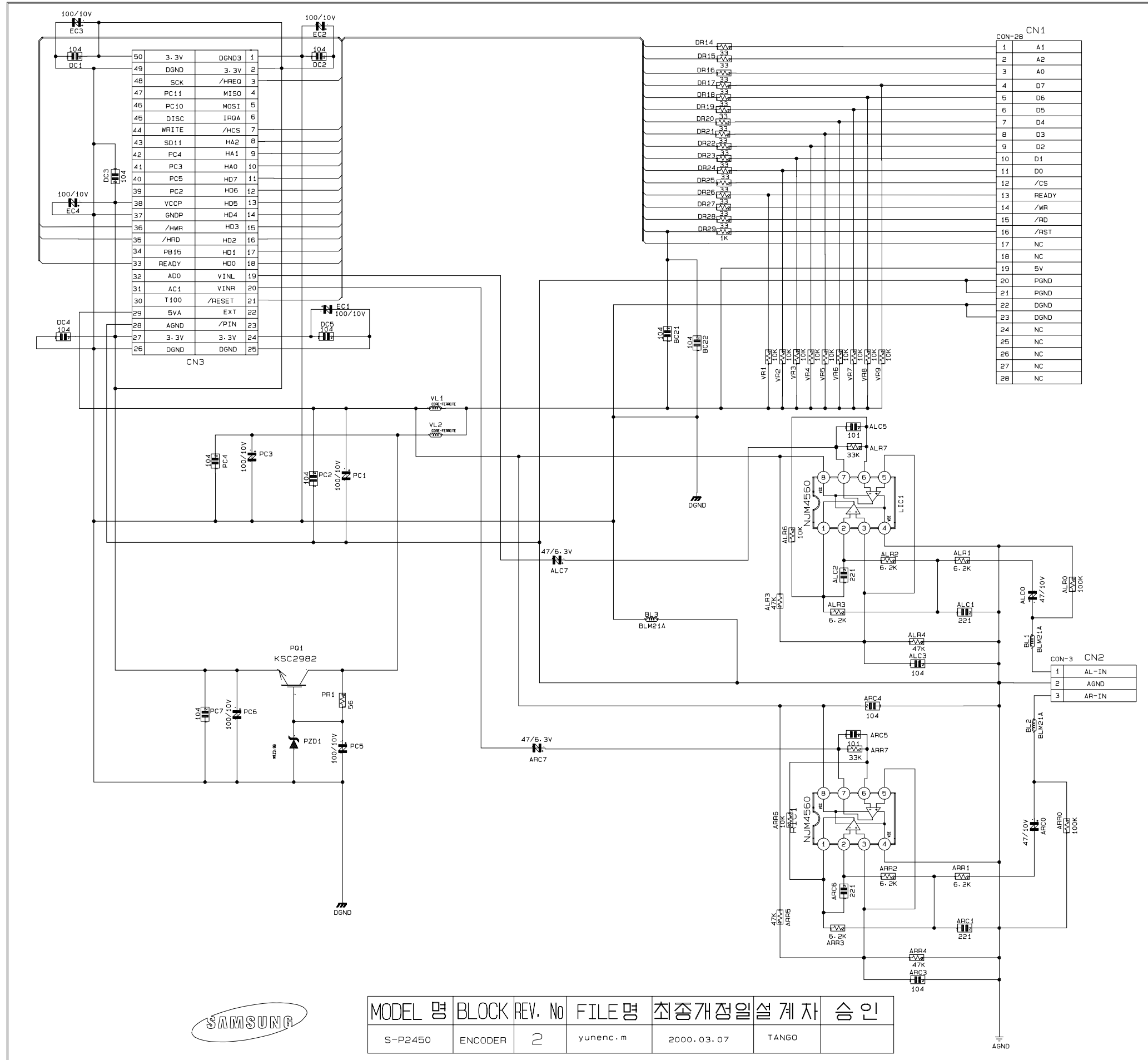




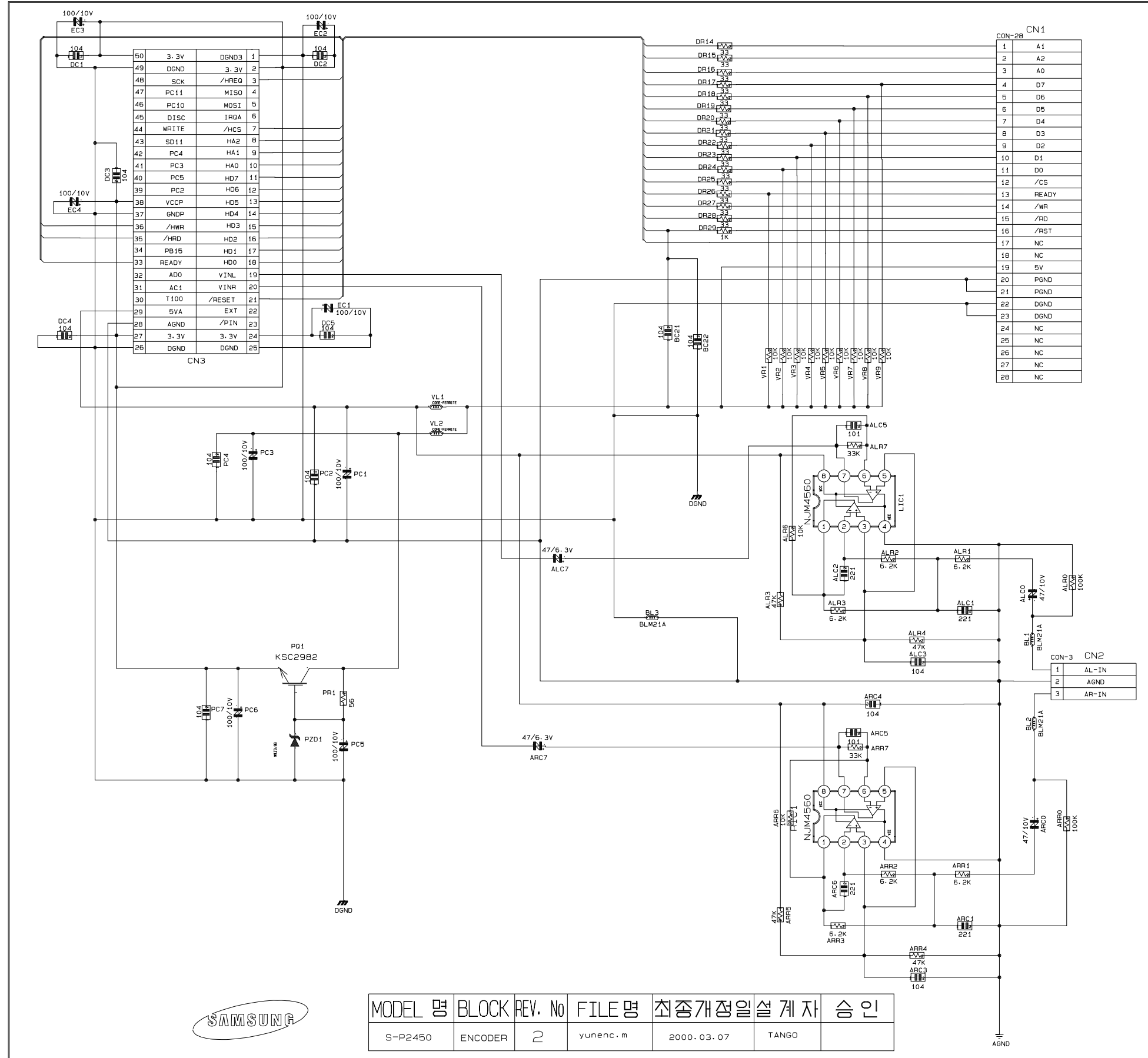


MODEL 명	BLOCK REV. No	FILE 명	최종개발설계자	승인
G3SLIM	0	s2000	CCH	

8-3 ENCODER

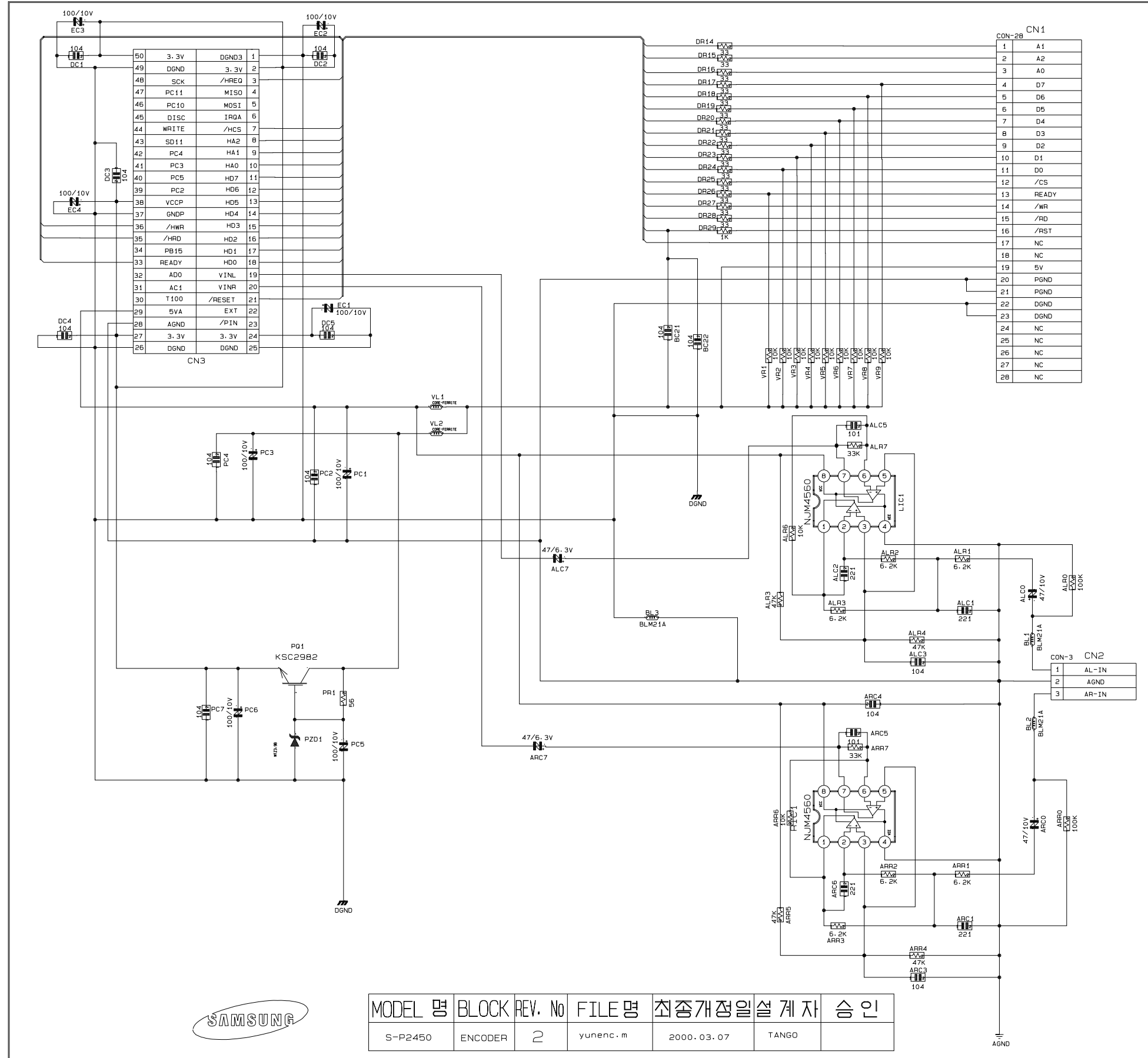


# ENCODER SCHEMATIC DIAGRAM (only S2450 Model)



MODEL 명	BLOCK	REV. No	FILE 명	최종개정일	설계자	승인
S-P2450	ENCODER	2	yunenc.m	2000.03.07	TANGO	

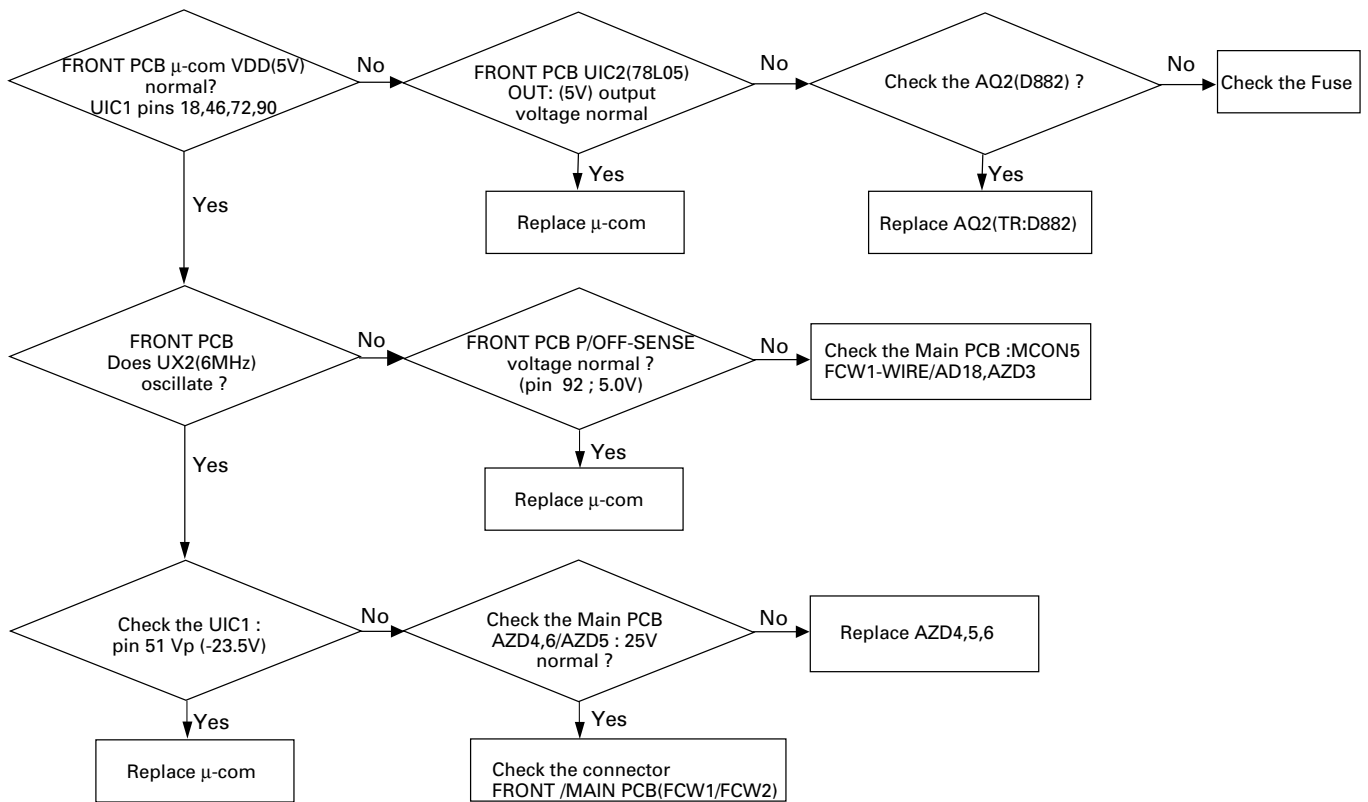
# ENCODER SCHEMATIC DIAGRAM (only S2450 Model)



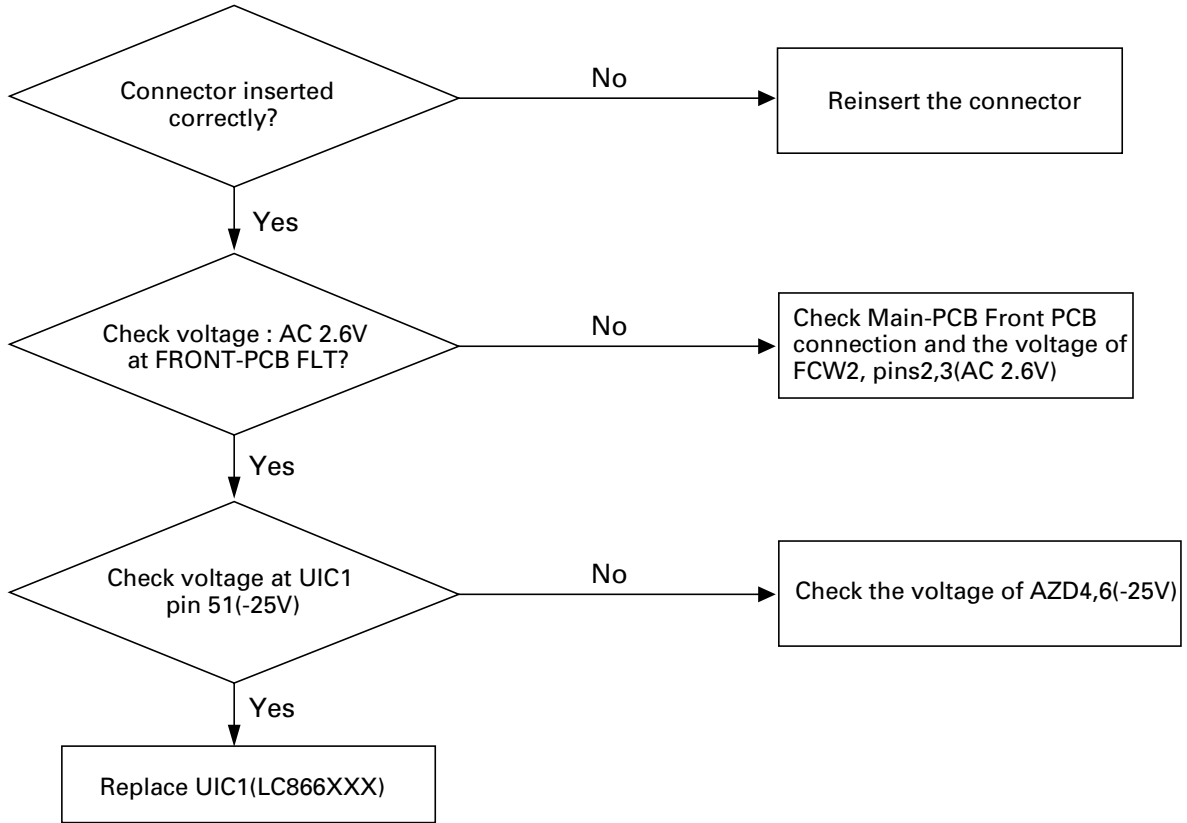
# 9. Troubleshooting

## 9-1 Amplifier

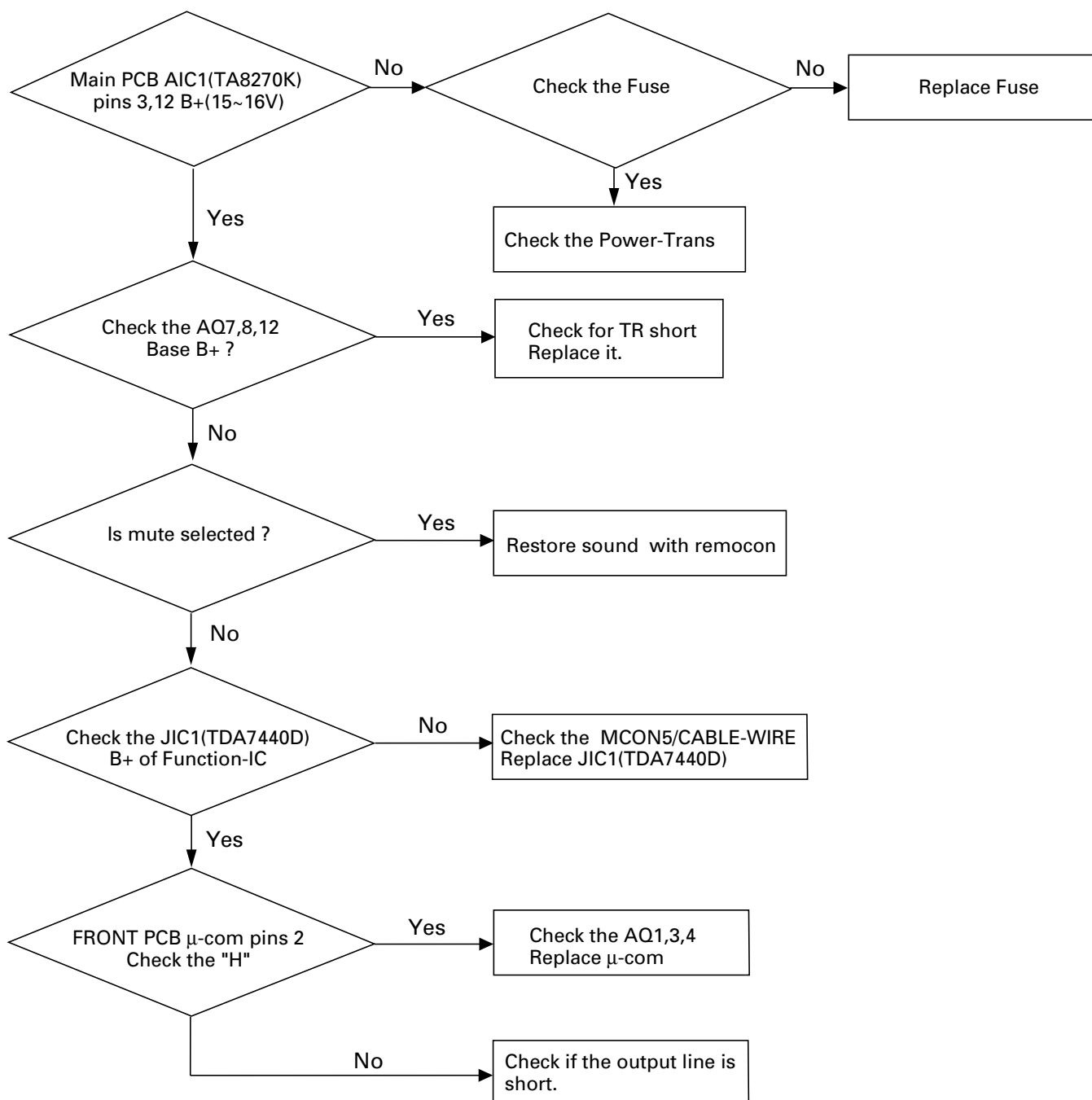
### 9-1-1 Power Malfunction



### 9-1-2 FLT Malfunction

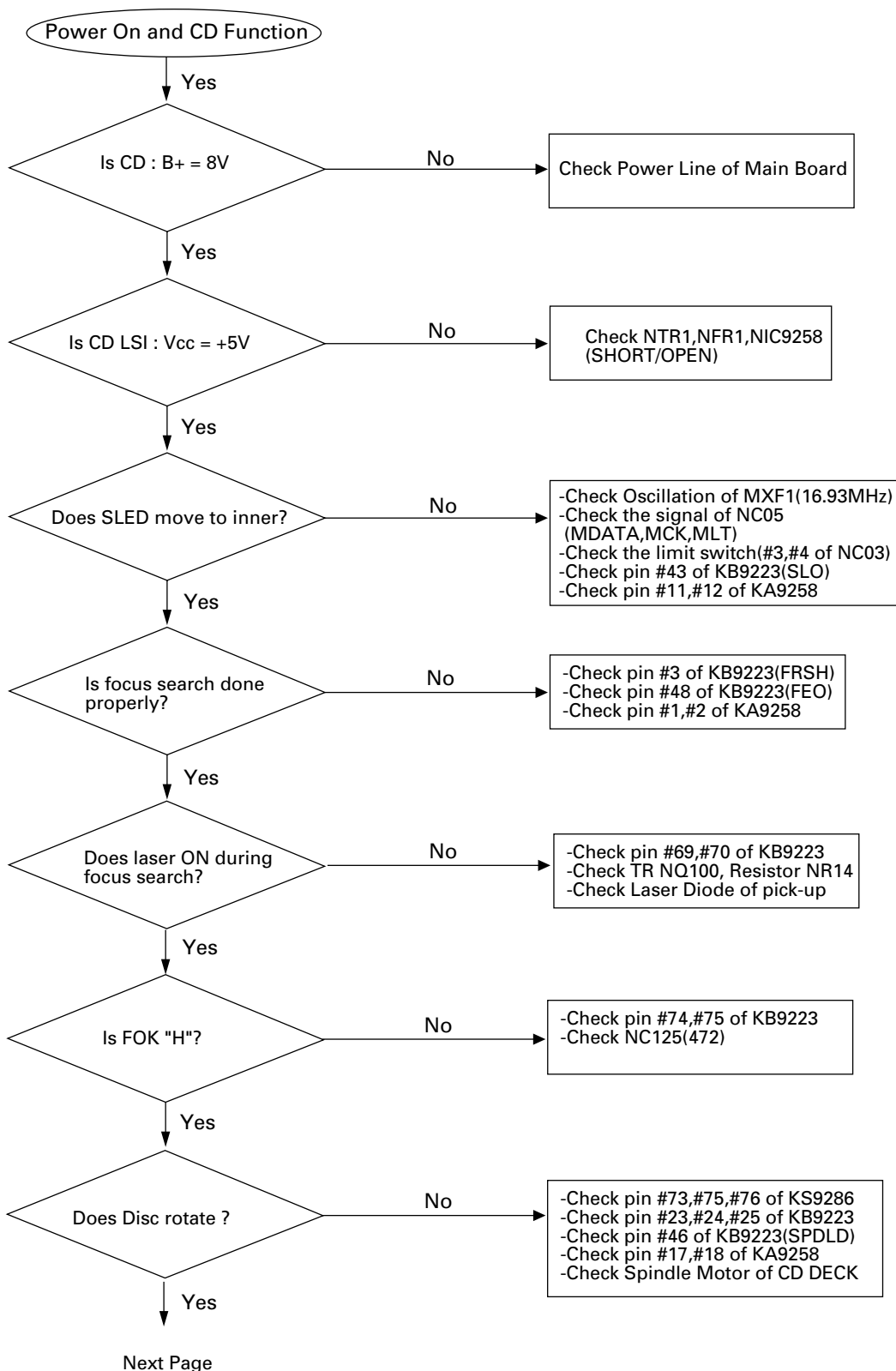


### 9-1-3 No Output



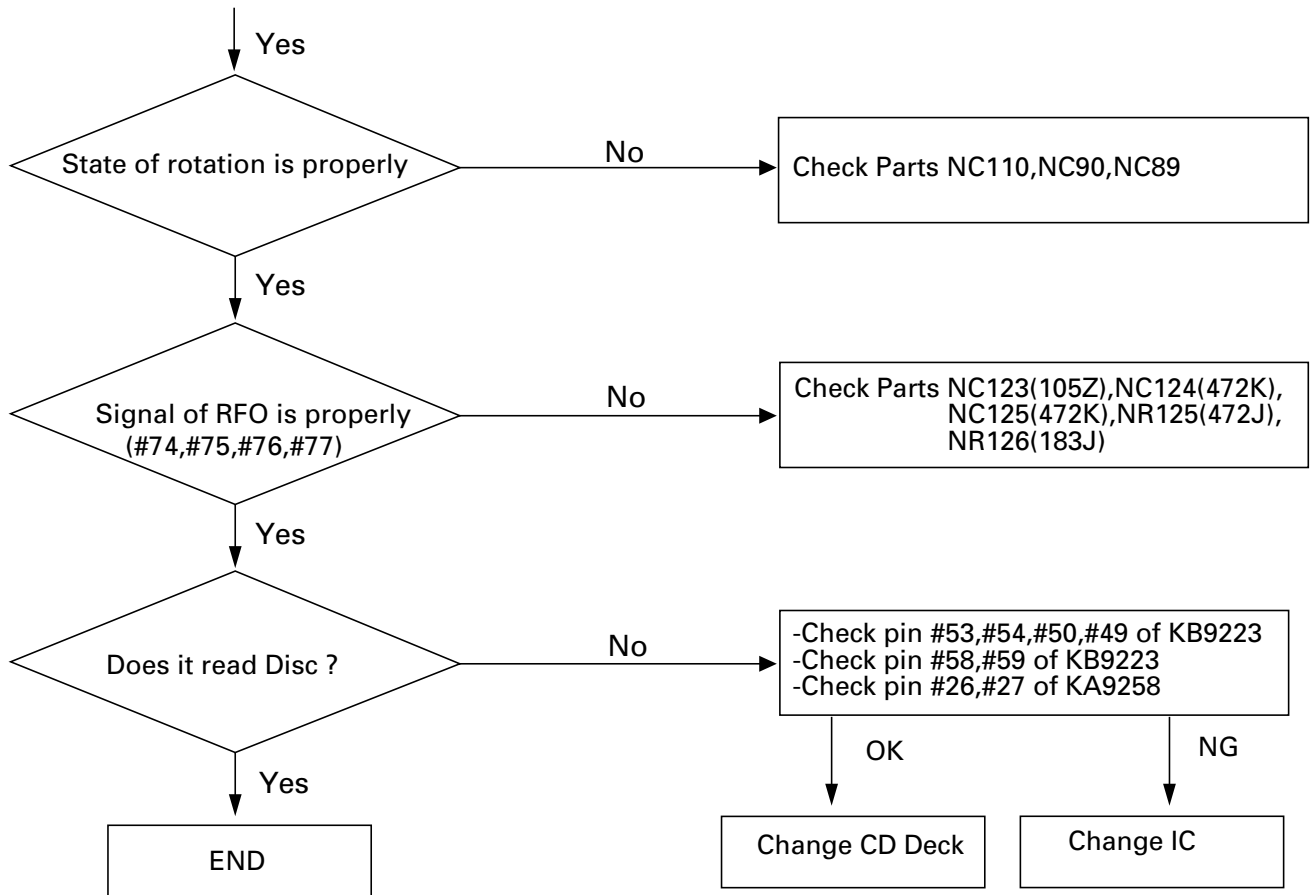
## 9-2 CD

### 9-2-1 Disc Revolution Malfunction



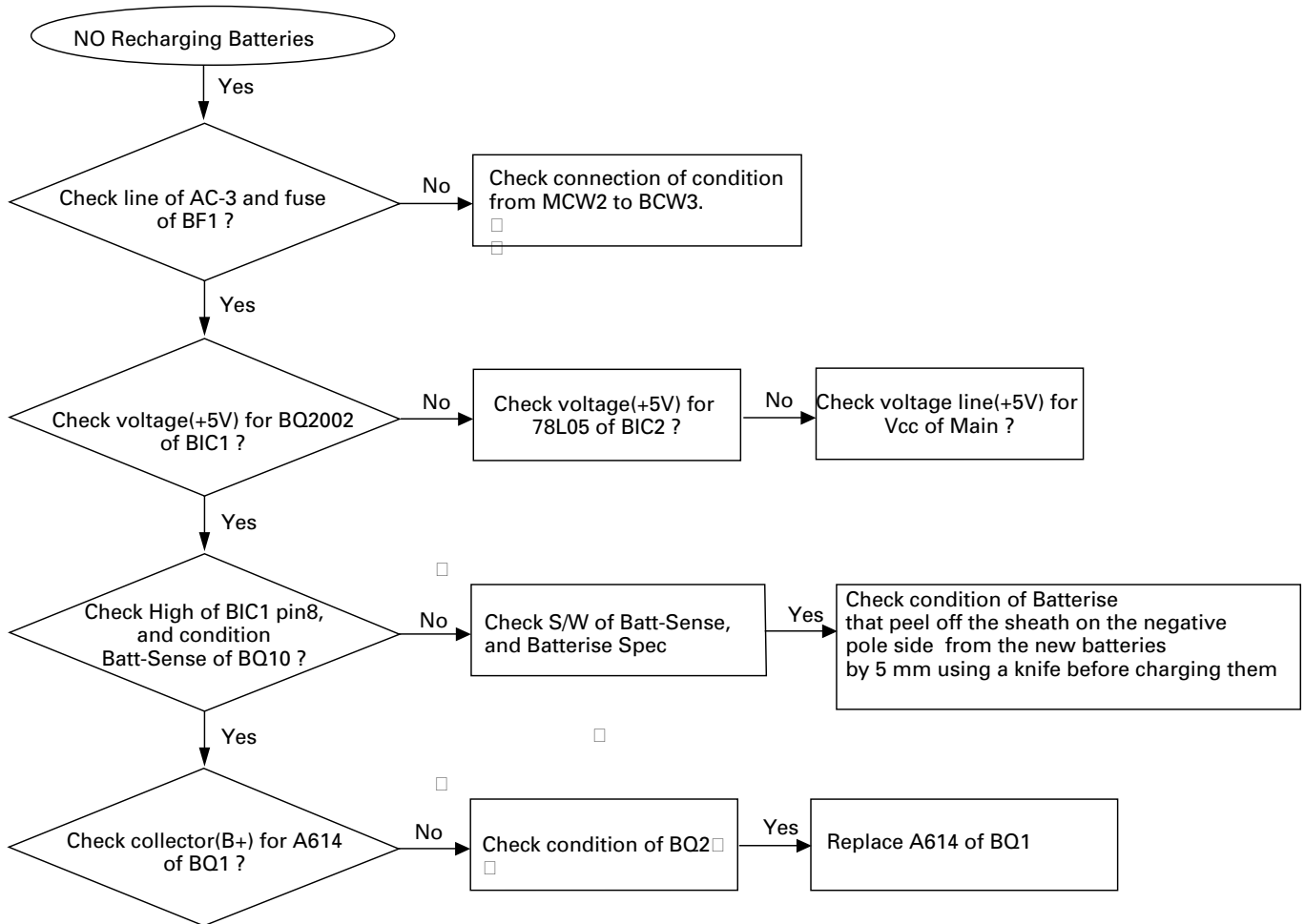


Troubleshooting

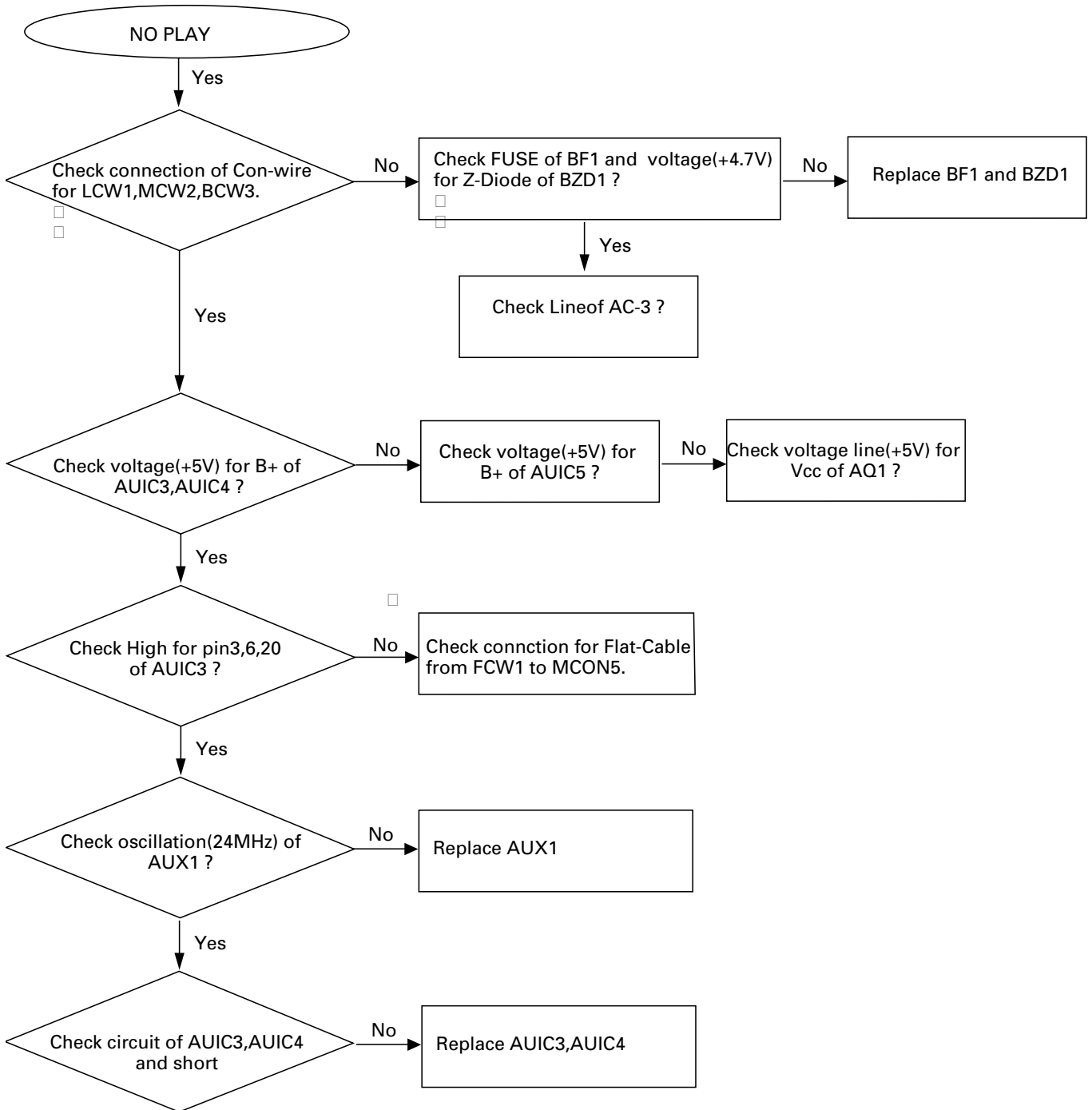


## 9-3 MP3

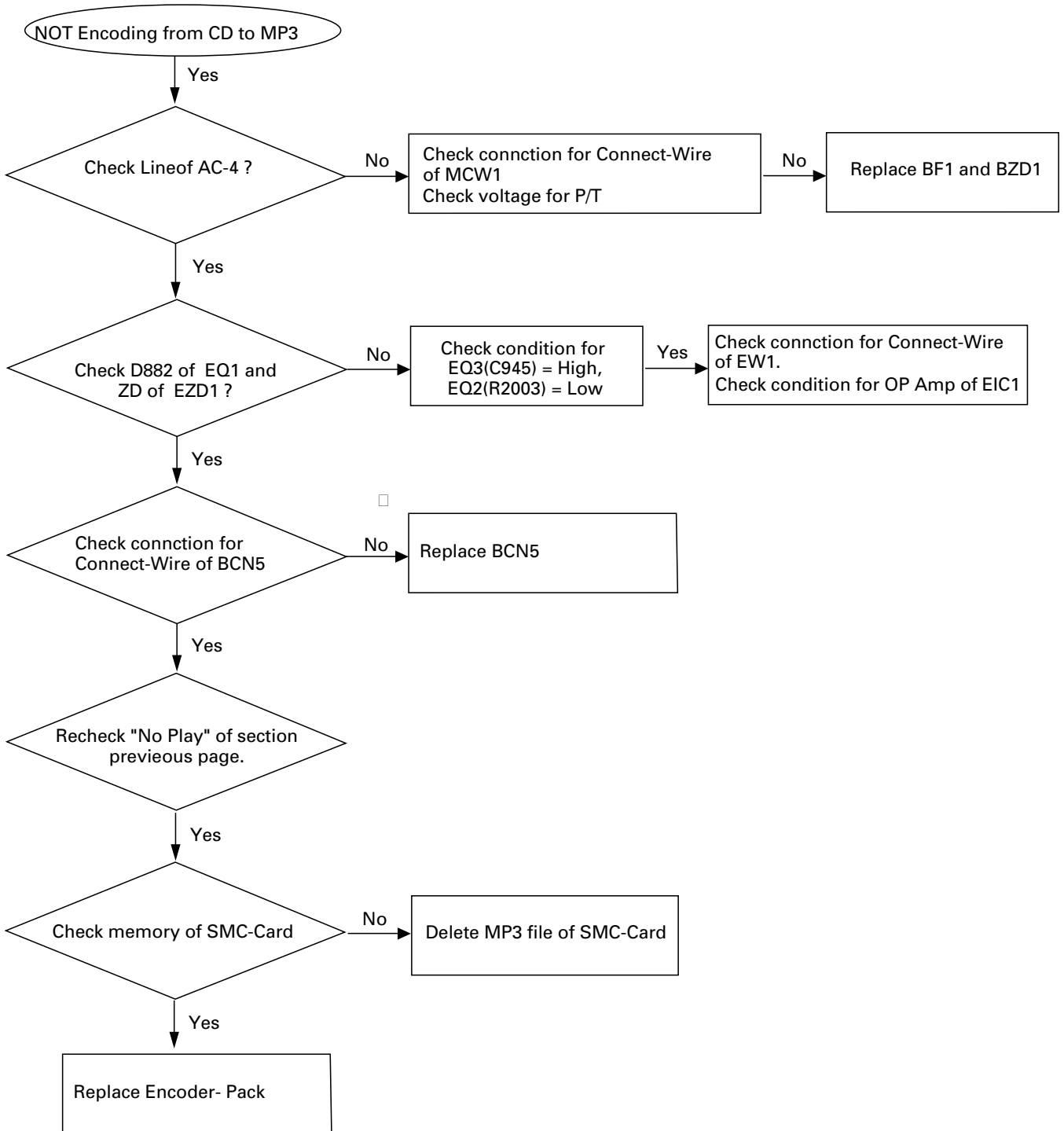
### 9-3-1 Recharge



### 9-3-2 No play



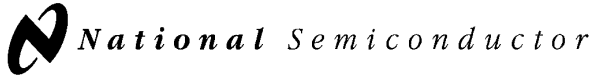
### 9-3-3 Encoding ;Only S-2450



# 10. IC Internal Diagram

## 10-1 74HCT245 ; AUIC4

MM74HCT245 Octal TRI-STATE Transceiver



August 1996

### MM74HCT245 Octal TRI-STATE® Transceiver

#### General Description

This TRI-STATE bi-directional buffer utilizes advanced silicon-gate CMOS technology and is intended for two-way asynchronous communication between data buses. It has high drive current outputs which enable high speed operation even when driving large bus capacitances. This circuit possesses the low power consumption of CMOS circuitry, yet has speeds comparable to low power Schottky TTL circuits.

This device is TTL input compatible and can drive up to 15 LS-TTL loads, and all inputs are protected from damage due to static discharge by diodes to V<sub>CC</sub> and ground.

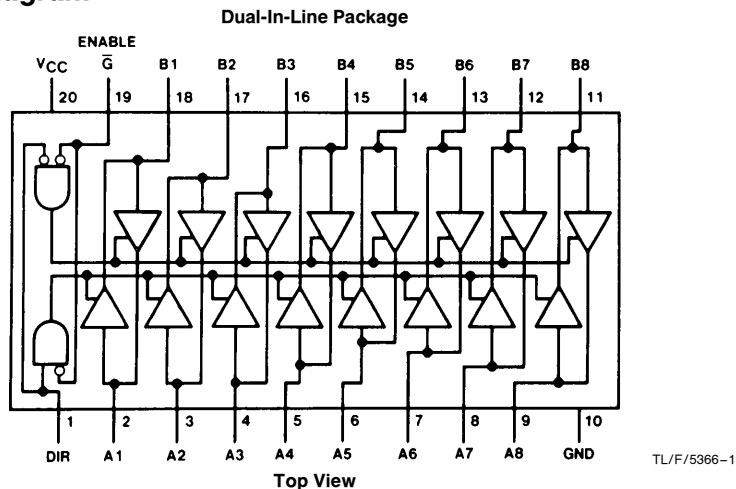
The MM74HCT245 has one active low enable input ( $\bar{G}$ ), and a direction control (DIR). When the DIR input is high, data flows from the A inputs to the B outputs. When DIR is low, data flows from B to A.

MM74HCT devices are intended to interface between TTL and NMOS components and standard CMOS devices. These parts are also plug-in replacements for LS-TTL devices and can be used to reduce power consumption in existing designs.

#### Features

- TTL input compatible
- TRI-STATE outputs for connection to system busses
- High output drive current: 6 mA (min)
- High speed: 16 ns typical propagation delay
- Low power: 80  $\mu$ A (74HCT Series)

#### Connection Diagram



Order Number MM74HCT245

#### Truth Table

Control Inputs		Operation
$\bar{G}$	DIR	
		245
L	L	B data to A bus
L	H	A data to B bus
H	X	isolation

H = high level L = low level, X = irrelevant

TRI-STATE® is a registered trademark of National Semiconductor Corp.

**Absolute Maximum Ratings** (Notes 1 & 2)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage ( $V_{CC}$ )	-0.5 to +7.0V
DC Input Voltage ( $V_{IN}$ )	-1.5 to $V_{CC} + 1.5V$
DC Output Voltage ( $V_{OUT}$ )	-0.5 to $V_{CC} + 0.5V$
Clamp Diode Current ( $I_{IK}, I_{OK}$ )	$\pm 20$ mA
DC Output Current, per pin ( $I_{OUT}$ )	$\pm 35$ mA
DC $V_{CC}$ or GND Current, per pin ( $I_{CC}$ )	$\pm 70$ mA
Storage Temperature Range ( $T_{STG}$ )	-65°C to +150°C
Power Dissipation ( $P_D$ )	
(Note 3)	600 mW
S.O. Package only	500 mW
Lead Temperature ( $T_L$ )	
(Soldering 10 seconds)	260°C

**Operating Conditions**

	Min	Max	Units
Supply Voltage ( $V_{CC}$ )	4.5	5.5	V
DC Input or Output Voltage ( $V_{IN}, V_{OUT}$ )	0	$V_{CC}$	V
Operating Temp. Range ( $T_A$ ) MM74HCT	-40	+85	°C
Input Rise or Fall Times ( $t_r, t_f$ )		500	ns

**DC Electrical Characteristics** ( $V_{CC} = 5V \pm 10\%$ , unless otherwise specified.)

Symbol	Parameter	Conditions	$T_A = 25^\circ C$		74HCT	$T_A = 125^\circ C$	Units
			Typ	Guaranteed Limits			
$V_{IH}$	Minimum High Level Input Voltage			2.0	2.0	2.0	V
$V_{IL}$	Maximum Low Level Input Voltage			0.8	0.8	0.8	V
$V_{OH}$	Minimum High Level Output Voltage	$V_{IN} = V_{IH}$ or $V_{IL}$ $ I_{OUT}  = 20 \mu A$	$V_{CC}$	$V_{CC} - 0.1$	$V_{CC} - 0.1$	$V_{CC} - 0.1$	V
		$ I_{OUT}  = 6.0$ mA, $V_{CC} = 4.5V$	4.2	3.98	3.84	3.7	V
		$ I_{OUT}  = 7.2$ mA, $V_{CC} = 5.5V$	5.2	4.98	4.84	4.7	V
$V_{OL}$	Maximum Low Level Voltage	$V_{IN} = V_{IH}$ or $V_{IL}$ $ I_{OUT}  = 20 \mu A$	0	0.1	0.1	0.1	V
		$ I_{OUT}  = 6.0$ mA, $V_{CC} = 4.5V$	0.2	0.26	0.33	0.4	V
		$ I_{OUT}  = 7.2$ mA, $V_{CC} = 5.5V$	0.2	0.26	0.33	0.4	V
$I_{IN}$	Maximum Input Current	$V_{IN} = V_{CC}$ or GND, $V_{IH}$ or $V_{IL}$ , Pin 1 or 19		$\pm 0.1$	$\pm 1.0$	$\pm 1.0$	$\mu A$
$I_{OZ}$	Maximum TRI-STATE Output Leakage Current	$V_{OUT} = V_{CC}$ or GND $\bar{G} = V_{IH}$		$\pm 0.5$	$\pm 5.0$	$\pm 10$	$\mu A$
$I_{CC}$	Maximum Quiescent Supply Current	$V_{IN} = V_{CC}$ or GND $I_{OUT} = 0 \mu A$		8	80	160	$\mu A$
		$V_{IN} = 2.4V$ or 0.5V (Note 4)	0.6	1.0	1.3	1.5	mA

**Note 1:** Absolute Maximum Ratings are those values beyond which damage to the device may occur.

**Note 2:** Unless otherwise specified all voltages are referenced to ground.

**Note 3:** Power Dissipation temperature derating — plastic "N" package: -12 mW/°C from 65°C to 85°C.

**Note 4:** Measured per input. All other inputs at  $V_{CC}$  or ground.

**AC Electrical Characteristics** MM74HCT245

$V_{CC} = 5.0V$ ,  $t_r = t_f = 6$  ns,  $T_A = 25^\circ C$  (unless otherwise specified)

Symbol	Parameter	Conditions	Typ	Guaranteed Limit	Units
$t_{PHL}$ , $t_{PLH}$	Maximum Output Propagation Delay	$C_L = 45$ pF	16	20	ns
$t_{PZL}$ , $t_{PZH}$	Maximum Output Enable Time	$C_L = 45$ pF $R_L = 1$ k $\Omega$	29	40	ns
$t_{PLZ}$ , $t_{PHZ}$	Maximum Output Disable Time	$C_L = 5$ pF $R_L = 1$ k $\Omega$	20	25	ns

**AC Electrical Characteristics** MM74HCT245

$V_{CC} = 5.0V \pm 10\%$ ,  $t_r = t_f = 6$  ns (unless otherwise specified)

Symbol	Parameter	Conditions	$T_A = 25^\circ C$		74HCT	$T_A = 125^\circ C$	Units
			Typ	Guaranteed Limits		$T_A = -40$ to $85^\circ C$	
$t_{PHL}$ , $t_{PLH}$	Maximum Output Propagation Delay	$C_L = 50$ pF	17	23	29	34	ns
		$C_L = 150$ pF	24	30	38	45	ns
$t_{PZL}$	Maximum Output Enable Time	$R_L = 1$ k $\Omega$ $C_L = 50$ pF	31	42	53	63	ns
$t_{PZH}$	Maximum Output Enable Time	$R_L = 1$ k $\Omega$ $C_L = 50$ pF	23	33	41	49	ns
$t_{PHZ}$ , $t_{PLZ}$	Maximum Output Disable Time	$R_L = 1$ k $\Omega$ $C_L = 50$ pF	21	30	38	45	ns
$t_{THL}$ , $t_{TLH}$	Maximum Output Rise and Fall Time	$C_L = 50$ pF	8	12	15	18	ns
$C_{IN}$	Maximum Input Capacitance		10	15	15	15	pF
$C_{OUT}$	Maximum Output/Input Capacitance		20	25	25	25	pF
$C_{PD}$	Power Dissipation Capacitance	(Note 5) $\bar{G} = V_{CC}$ $\bar{G} = GND$	7 100				pF pF

**Note 5:**  $C_{PD}$  determines the no load power consumption,  $P_D = C_{PD} V_{CC}^2 f + I_{CC} V_{CC}$ , and the no load dynamic current consumption,  $I_S = C_{PD} V_{CC} f + I_{CC}$ .

## 10-2 BA3121N ; AUIC2

### Audio ICs

# Ground isolation amplifier

## BA3121 / BA3121F / BA3121N

The BA3121, BA3121F and BA3121N are ground isolation amplifiers developed for use in car audio applications. These ICs efficiently eliminate problems caused by wiring resistance, and remove noise generated by the electrical devices used in automobiles. The capacitance values of the external capacitors required for the ICs are small to allow compact and reliable set design.

#### ●Applications

Car audio systems

#### ●Features

- 1) Large capacitors not required
- 2) High common-mode rejection ratio (57dB typ. at f = 1kHz).
- 3) Low noise ( $V_{NO} = 3.5\mu V_{rms}$  Typ.).
- 4) Low distortion (THD = 0.002% Typ.).
- 5) Two channels.

#### ●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	V <sub>cc</sub>	18	V
Power dissipation	P <sub>d</sub>	800 (BA3121) *	mW
		450 (BA3121F) *	
		900 (BA3121N) *	
Operating temperature	T <sub>opr</sub>	-30~+85	°C
Storage temperature	T <sub>stg</sub>	-55~+125	°C

\* Reduced by 8.0mW (BA3121), 4.5mW (BA3121F), and 9.0mW (BA3121N) for each increase in Ta of 1°C over 25°C.

#### ●Recommended operating conditions (Ta = 25°C)

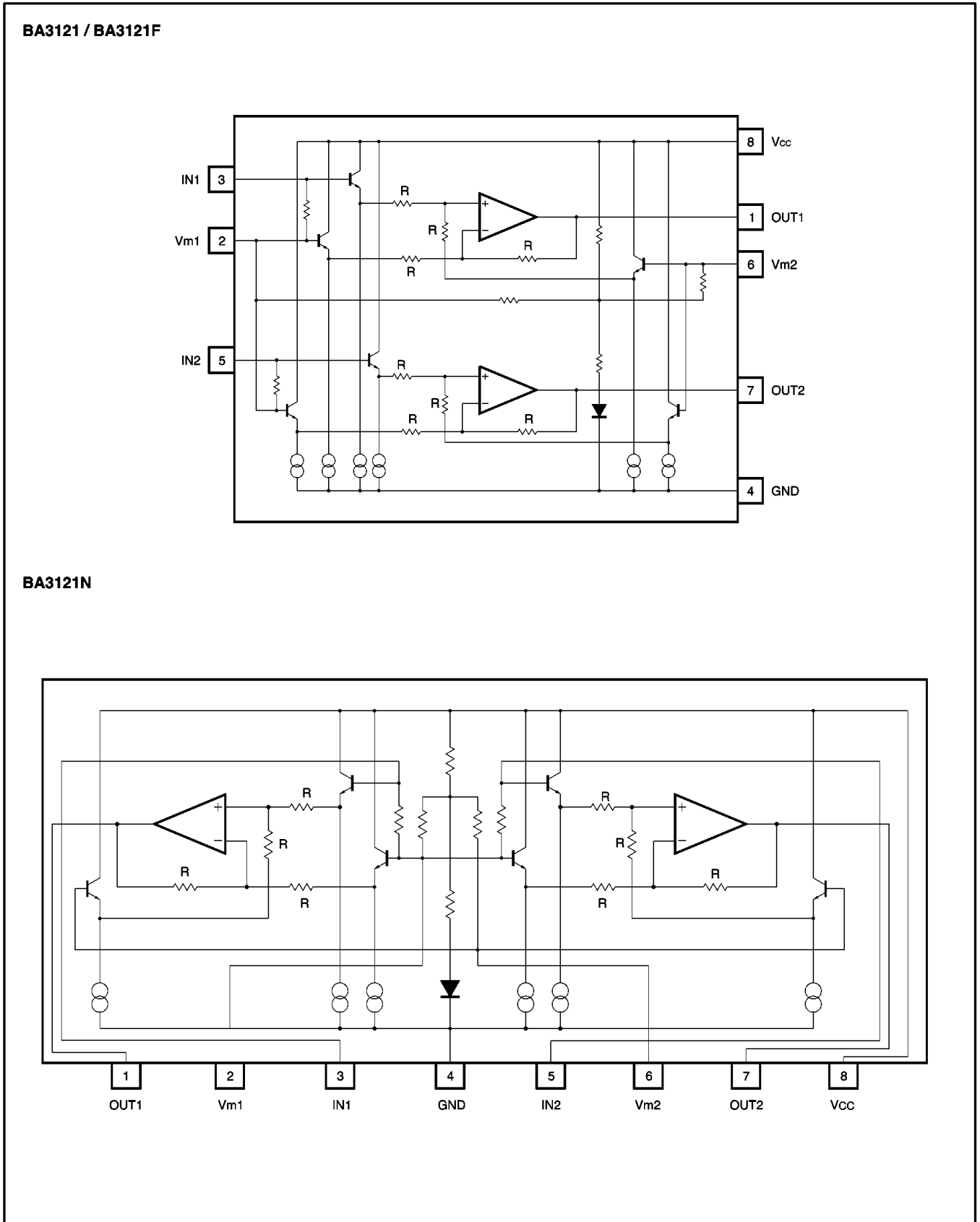
Parameter	Symbol	Min.	Typ.	Max.	Unit
Power supply voltage	V <sub>cc</sub>	4	12	18	V



Audio ICs

BA3121 / BA3121F / BA3121N

● Block diagrams



## Audio ICs

## BA3121 / BA3121F / BA3121N

●Electrical characteristics (unless otherwise noted,  $T_a = 25^\circ\text{C}$ ,  $V_{CC} = 12\text{V}$ ,  $f = 1\text{kHz}$ ,  $R_g = 1.8\text{k}\Omega$ )

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Quiescent current	$I_Q$	5.6	9.0	14.0	mA	$V_{IN}=0V_{rms}$
Output noise voltage	$V_{NO}$	—	3.5	8.0	$\mu V_{rms}$	BPF=20Hz~20kHz
Voltage gain	$G_V$	-1.5	-0.04	1.5	dB	$V_O=-10\text{dBm}$ , $R_g=0\Omega$
Maximum output voltage	$V_{OM}$	1.8	2.0	—	$V_{rms}$	THD=0.1%, $V_{CC}=8\text{V}$
Total harmonic distortion	THD	—	0.002	0.02	%	$V_O=0.7V_{rms}$
Common-mode rejection ratio	CMRR	41	57	—	dB	
Common-mode voltage	$V_{CM}$	2.5	3.75	—	$V_{rms}$	$V_{CC}=8\text{V}$ , CMRR=40dB
Ripple rejection ratio	RR	72	80	—	dB	$f_{RR}=100\text{Hz}$ , $V_{RR}=-10\text{dBm}$ , $R_g=0\Omega$
Channel separation	CS	—	82	—	dB	$V_{IN}=-10\text{dBm}$ , $R_g=1.8\text{k}\Omega/\text{OPEN}$
Slew rate	SR	—	2.0	—	$\text{V}/\mu\text{s}$	
Input resistance	$R_{IN}$	44	55	66	$\text{k}\Omega$	

©Not designed for radiation resistance.

**NiCd/NiMH Fast-Charge Management ICs**

**Features**

- Fast charge of nickel cadmium or nickel-metal hydride batteries
- Direct LED output displays charge status
- Fast-charge termination by  $-\Delta V$ , maximum voltage, maximum temperature, and maximum time
- Internal band-gap voltage reference
- Optional top-off charge
- Selectable pulse trickle charge rates
- Low-power mode
- 8-pin 300-mil DIP or 150-mil SOIC

**General Description**

The bq2002 and bq2002/F Fast-Charge ICs are low-cost CMOS battery-charge controllers providing reliable charge termination for both NiCd and NiMH battery applications. Controlling a current-limited or constant-current supply allows the bq2002/F to be the basis for a cost-effective stand-alone or system-integrated charger. The bq2002/F integrates fast charge with optional top-off and pulsed-trickle control in a single IC for charging one or more NiCd or NiMH battery cells.

Fast charge is initiated on application of the charging supply or battery replacement. For safety, fast charge is inhibited if the battery temperature and voltage are outside configured limits.

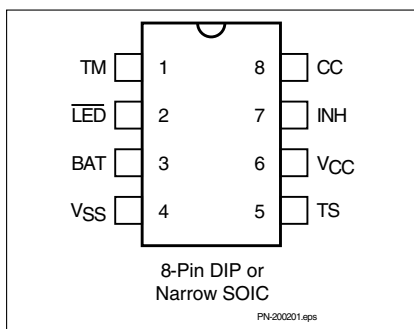
Fast charge is terminated by any of the following:

- Peak voltage detection (PVD)
- Negative delta voltage ( $-\Delta V$ )
- Maximum voltage
- Maximum temperature
- Maximum time

After fast charge, the bq2002/F optionally tops-off and pulse-trickles the battery per the pre-configured limits. Fast charge may be inhibited using the INH pin. The bq2002/F may also be placed in low-standby-power mode to reduce system power consumption.

The bq2002F differs from the bq2002 only in that a slightly different set of fast-charge and top-off time limits is available. All differences between the two ICs are illustrated in Table 1.

**Pin Connections**



**Pin Names**

TM	Timer mode select input	TS	Temperature sense input
$\overline{\text{LED}}$	Charging status output	$V_{CC}$	Supply voltage input
BAT	Battery voltage input	INH	Charge inhibit input
$V_{SS}$	System ground	CC	Charge control output

**bq2002/F Selection Guide**

Part No.	TCO	HTF	LTF	$-\Delta V$	PVD	Fast Charge	$t_{MTO}$	Top-Off	Maintenance
bq2002	$0.5 * V_{CC}$	None	None		✓	C/2	160	C/32	C/64
					✓	1C	80	C/16	C/64
				✓		2C	40	None	C/32
bq2002F	$0.5 * V_{CC}$	None	None		✓	C/2	160	C/32	C/64
					✓	1C	100	C/16	C/64
				✓		2C	55	None	C/32

# bq2002/F

## Pin Descriptions

<b>TM</b>	<b>Timer mode input</b>  A three-level input that controls the settings for the fast charge safety timer, voltage termination mode, top-off, pulse-trickle, and voltage hold-off time.
<b><math>\overline{\text{LED}}</math></b>	<b>Charging output status</b>  Open-drain output that indicates the charging status.
<b>BAT</b>	<b>Battery input voltage</b>  The battery voltage sense input. The input to this pin is created by a high-impedance resistor divider network connected between the positive and negative terminals of the battery.
<b>VSS</b>	<b>System ground</b>
<b>TS</b>	<b>Temperature sense input</b>  Input for an external battery temperature monitoring thermistor.
<b>VCC</b>	<b>Supply voltage input</b>  5.0V $\pm$ 20% power input.
<b>INH</b>	<b>Charge inhibit input</b>  When high, INH suspends the fast charge in progress. When returned low, the IC resumes operation at the point where initially suspended.

## CC Charge control output

An open-drain output used to control the charging current to the battery. CC switching to high impedance (Z) enables charging current to flow, and low to inhibit charging current. CC is modulated to provide top-off, if enabled, and pulse trickle.

## Functional Description

Figure 2 shows a state diagram and Figure 3 shows a block diagram of the bq2002/F.

## Battery Voltage and Temperature Measurements

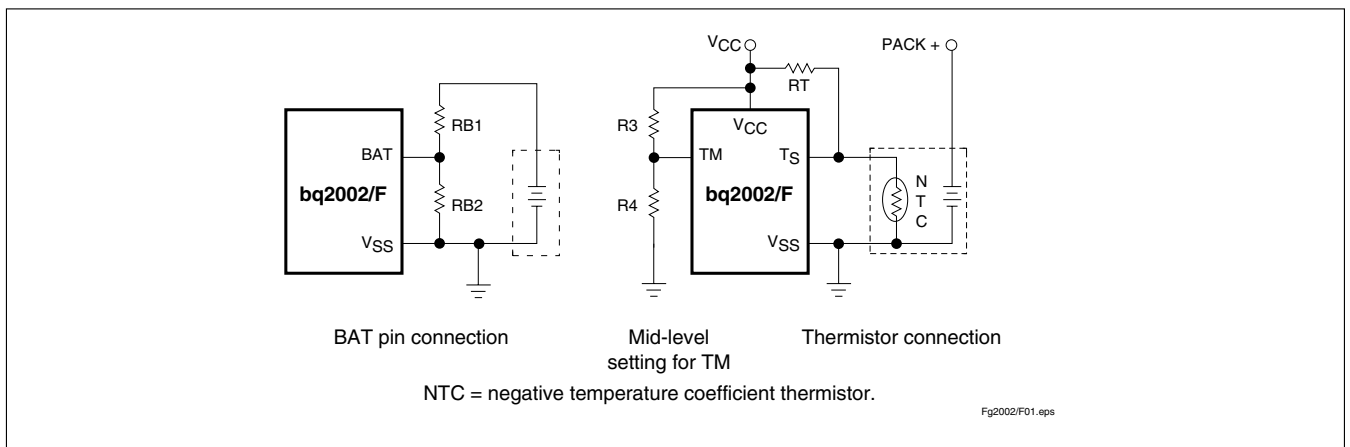
Battery voltage and temperature are monitored for maximum allowable values. The voltage presented on the battery sense input, BAT, should represent a single-cell potential for the battery under charge. A resistor-divider ratio of

$$\frac{RB1}{RB2} = N - 1$$

is recommended to maintain the battery voltage within the valid range, where N is the number of cells, RB1 is the resistor connected to the positive battery terminal, and RB2 is the resistor connected to the negative battery terminal. See Figure 1.

**Note:** This resistor-divider network input impedance to end-to-end should be at least 200k $\Omega$  and less than 1 M $\Omega$ .

A ground-referenced negative temperature coefficient thermistor placed near the battery may be used as a low-cost temperature-to-voltage transducer. The temperature sense voltage input at TS is developed using a resistor-thermistor network between VCC and VSS. See Figure 1.



**Figure 1. Voltage and Temperature Monitoring and TM Pin Configuration**

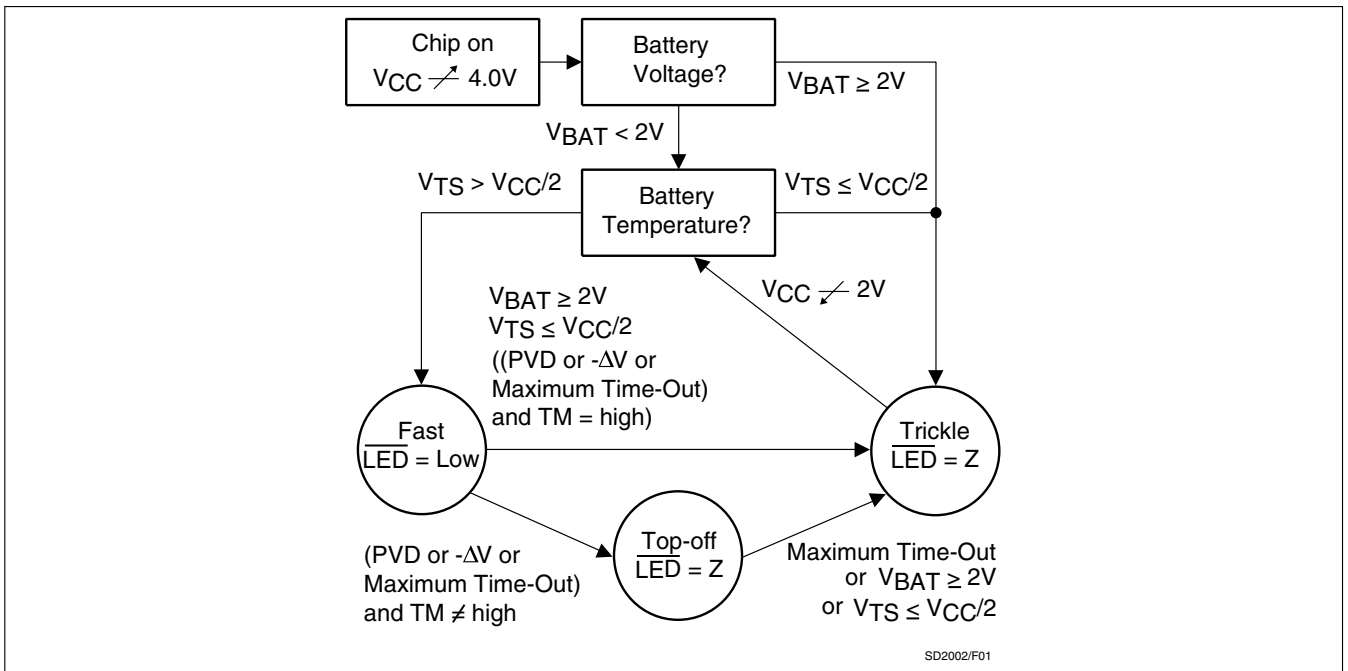


Figure 2. State Diagram

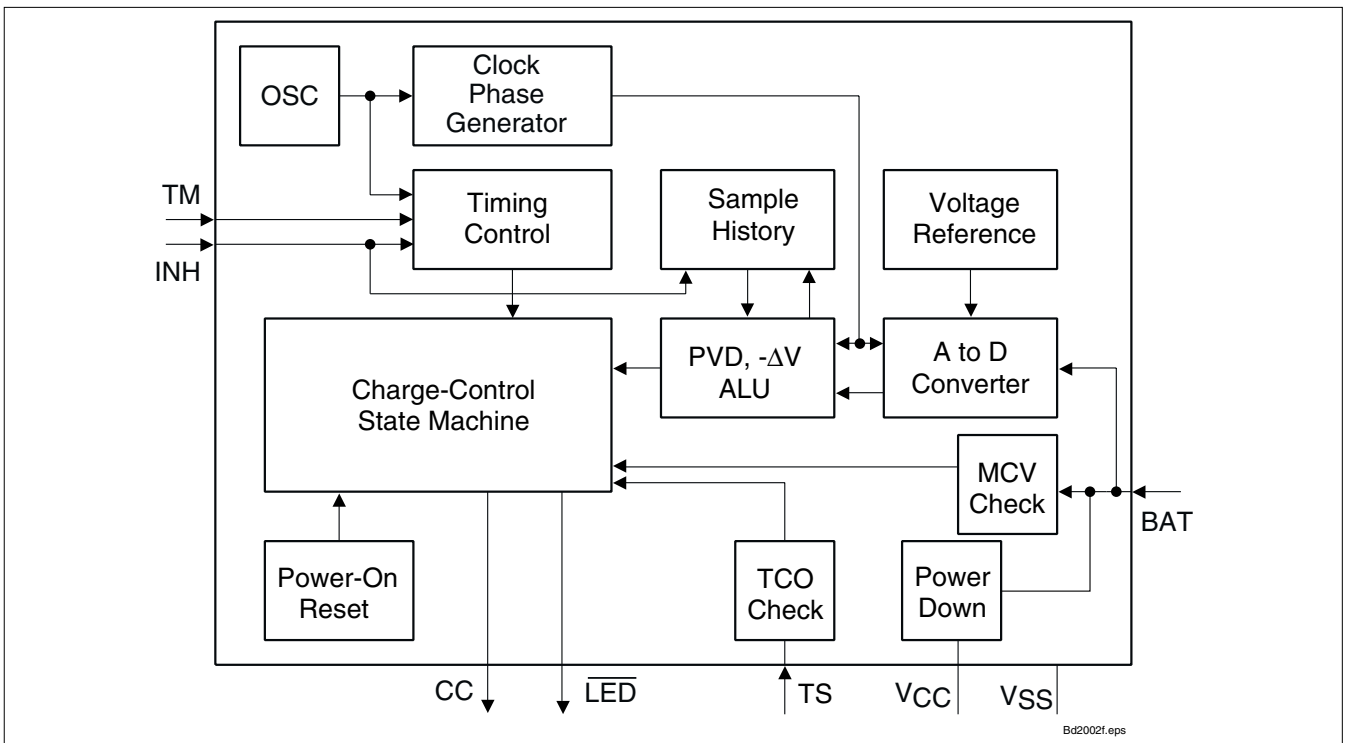


Figure 3. Block Diagram

## 10-4 BU1923F ; RIC1

### Audio ICs

# RDS / RBDS decoder

## BU1923 / BU1923F

The BU1923 and BU1923F are RDS / RBDS decoders that employ a digital PLL and have a built-in anti-aliasing filter and an eight-stage BPF (switched-capacitor filter). Linear CMOS circuitry is used for low power consumption.

#### ●Applications

RDS / RBDS compatible FM receivers for American and European markets, car stereos, high-fidelity stereo systems and components, and FM pagers.

#### ●Features

- 1) Low current.
- 2) Two-stage anti-aliasing filter (LPF).
- 3) 57kHz band-pass filter.
- 4) DSB demodulation (digital PLL).
- 5) Quality indication output for demodulated data.

#### ●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit	Conditions
Power supply voltage	V <sub>DD</sub>	-0.3~+7.0	V	V <sub>DD1</sub> V <sub>DD2</sub>
Maximum input voltage	V <sub>MAX</sub>	-0.3~V <sub>DD</sub> +0.3	V	All input pins
Maximum output voltage	I <sub>MAX</sub>	±4.0	mA	All output pins
Power dissipation	P <sub>d</sub>	350*	mW	—
Operating temperature	T <sub>opr</sub>	-40~+85	°C	—
Storage temperature	T <sub>stg</sub>	-55~+125	°C	—

\*Reduced by 3.5mW for each increase in Ta of 1°C over 25°C.

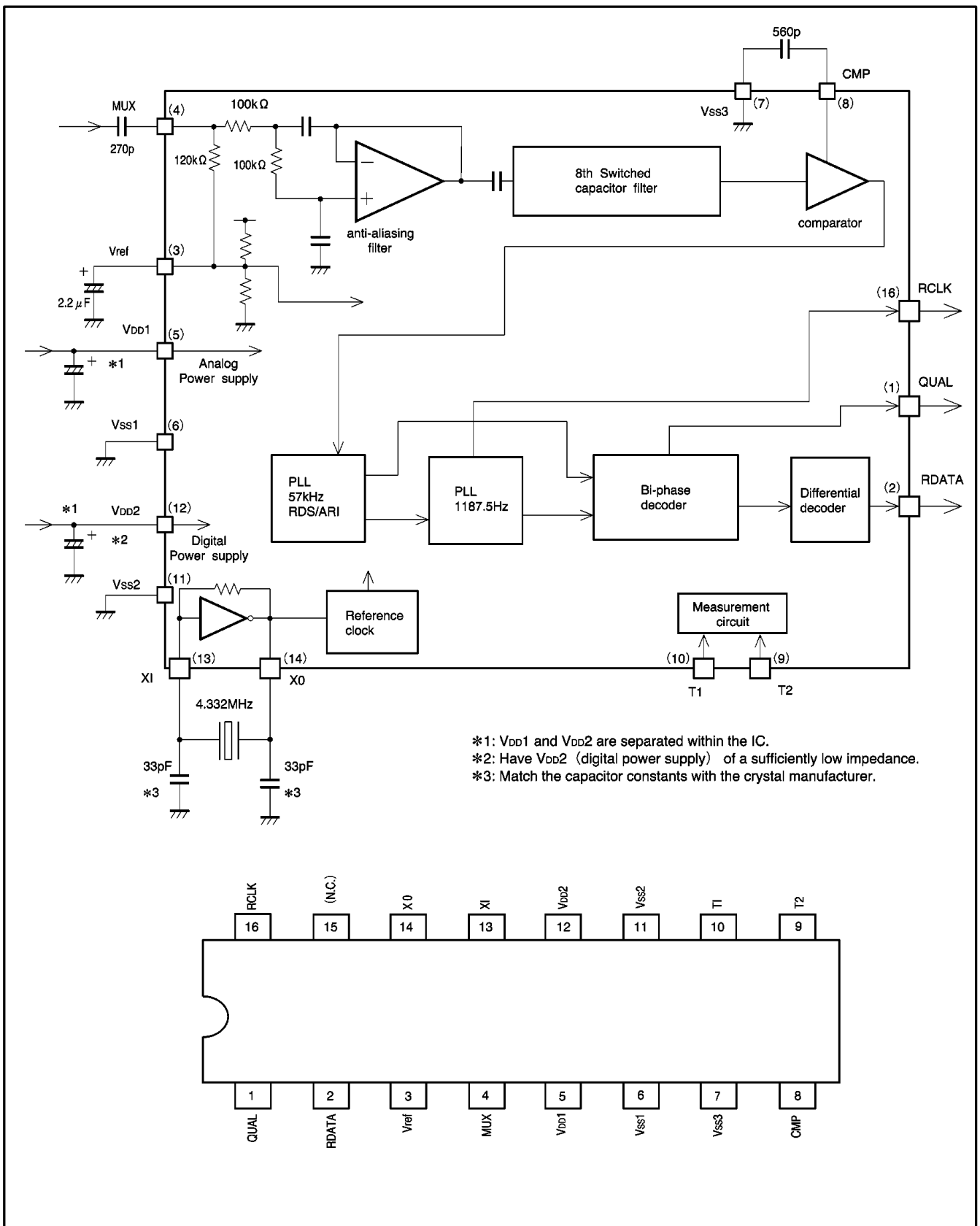
#### ●Recommended operating conditions (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Power supply voltage	V <sub>DD1</sub>	4.5	—	5.5	V
	V <sub>DD2</sub>	4.5	—	5.5	V

Audio ICs

BU1923 / BU1923F

● Block diagram



Audio ICs

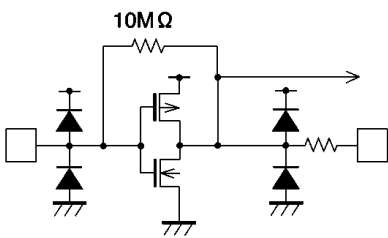
BU1923 / BU1923F

● Pin descriptions

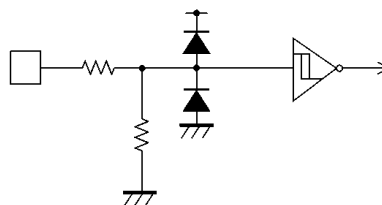
Pin No.	Symbol	Pin name	Function	Input/output type
1	QUAL	Demodulator quality	Good data: High, bad data: Low	Type C
2	RDATA	Demodulator data	Refer to output data timing	—
3	Vref	Reference voltage	1/2 V <sub>DD1</sub> (refer to input/output circuits)	Type E
4	MUX	Input	Composite signal input (refer to input/output circuits)	Type D
5	V <sub>DD1</sub>	Analog power supply	4.5V to 5.5V	—
6	V <sub>SS1</sub>			
7	CMP	Comparator input	C-junction (refer to input/output circuits )	Type D
8	V <sub>SS3</sub>	GND	—	—
9	T2	Test input	Open or connected to ground	Type B
10	T1			
11	V <sub>DD2</sub>	Digital power supply	4.5V to 5.5V	—
12	V <sub>SS2</sub>			
13	XI	Crystal oscillator	Connects to 4.332MHz oscillator (refer to input/output circuits)	Type A
14	XO			
15	(N.C.)	—	—	—
16	RCLK	Demodulator clock	1187.5Hz clock (refer to the timing diagram)	Type C

● Input / output circuits

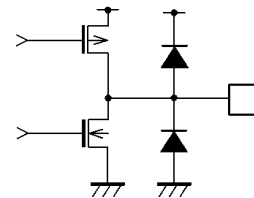
Type A



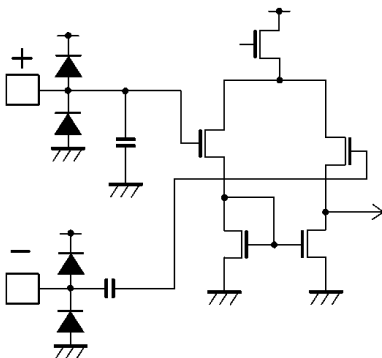
Type B



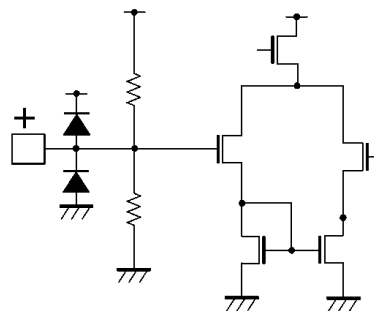
Type C



Type D



Type E





**LA1837**

**Single-Chip Home Stereo IC with Electronic Tuning Support**

**Overview**

The LA1837 is a single-chip AM/FM IF and MPX IC that supports electronic tuning and was developed for use in home stereo systems. It is optimal for use in automatic station selection systems that use the SD and IF counting techniques.

**Functions**

- AM: RF amplifier, mixer, oscillator, IF amplifier, detector AGC, oscillator buffer, S-meter, narrow-band SD, IF buffer
- FM IF: IF amplifier, quadrature detector, S-meter, S-curve detector, IF buffer output
- MPX: PLL stereo decoder, stereo display, forced mono, VCO stop, post amplifier, audio muting, adjacent channel interference rejection function

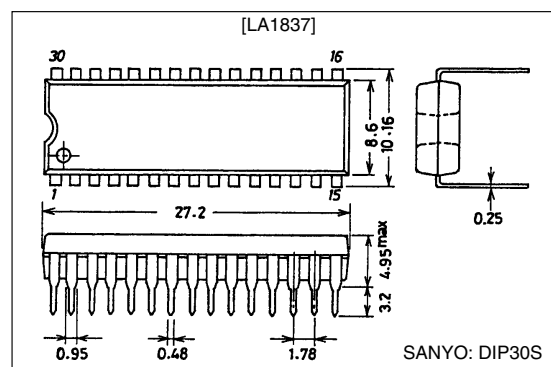
**Features**

- On-chip MPX VCO (no external components required)
- Adjacent channel interference rejection function (third and fifth order)
- Supports both the SD and the IF counting techniques.
- The AM and FM SD sensitivities can be set independently.
- The AM and FM output levels can be set independently.
- Improved AM practical sensitivity and high-input distortion characteristics

**Package Dimensions**

unit: mm

**3061-DIP30S**



**Specifications**

**Maximum Ratings at Ta = 25°C**

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		12	V
Allowable power dissipation	Pd max	Ta ≤ 70°C	550	mW
Operating temperature	Topr		-20 to +70	°C
Storage temperature	Tstg		-40 to +125	°C

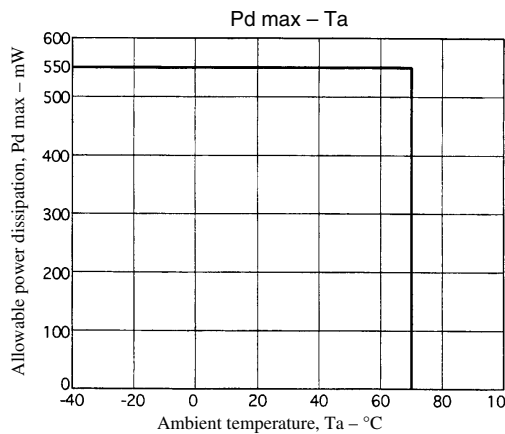
**Operating Conditions at Ta = 25°C**

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V <sub>CC</sub>		9	V
Operating voltage range	V <sub>CC</sub> op		7 to 11	V

LA1837

Electrical Characteristics at Ta = 25°C, VCC = 9 V, in the specified Test Circuit

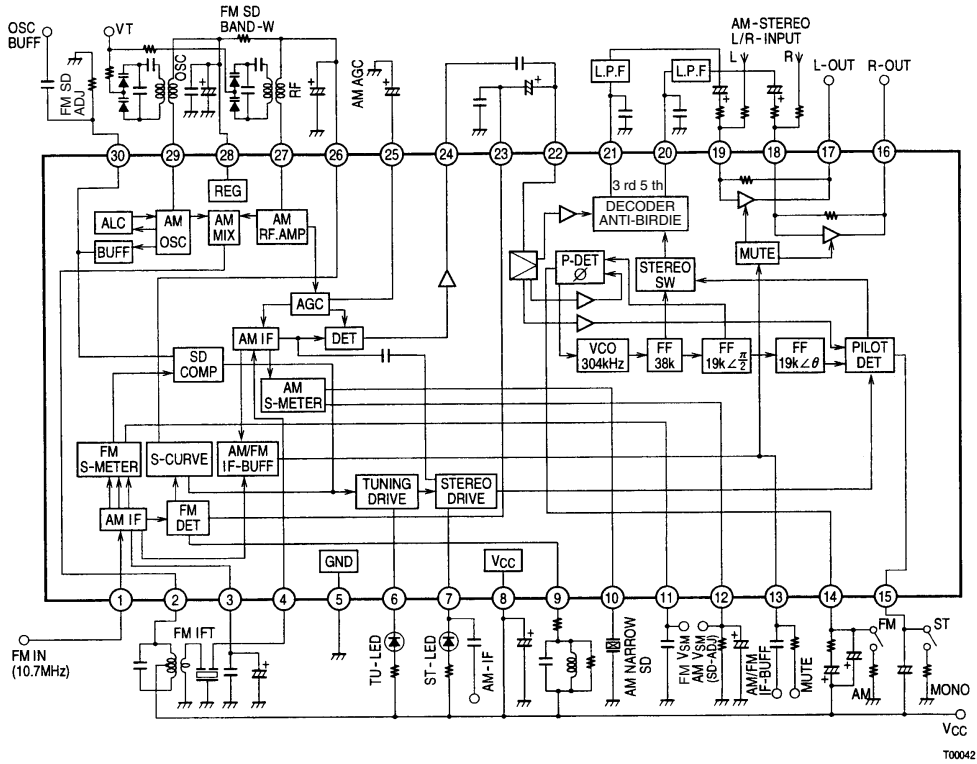
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
[FM Mono Characteristics] fc = 10.7 MHz, fm = 1 kHz, with the coil adjusted so that V <sub>AFC</sub> - V <sub>REG</sub> = 0 V						
Current drain	I <sub>CCO-FM</sub>	With no input	18	31	44	mA
Demodulation output	V <sub>OFM</sub>	100 dBμ, 100% mod. The pin 16 output	730	1100	1460	mVrms
Channel balance	C.B-mono	100 dBμ, 100% mod. The pin 16 output/pin 17 output	-1.5	0	+1.5	dB
Total harmonic distortion (mono)	THD <sub>FM(1)</sub>	100 dBμ, 100% mod. The pin 16 output		0.3	1.3	%
Total harmonic distortion (mono)	THD <sub>FM(2)</sub>	100 dBμ, 200% mod. The pin 16 output		1.0	5	%
Signal-to-noise ratio	S/N <sub>FM</sub>	100 dBμ, 100% mod. The pin 16 output	72	80		dB
AM rejection ratio	AMR	100 dBμ, AM 30 % mod. The pin 16 output	45	65		dB
Limiting input voltage	-3 dBLS	100 dBμ, 100% mod. Referenced to the output. The input such that the output is down -3 dB.	26	32	38	dBμ
LED sensitivity	SD <sub>On-FM</sub>		51	60	69	dBμ
LED bandwidth	SD <sub>BW</sub>	100 dBμ	85	120	170	kHz
IF count buffer output	V <sub>IFBuff-FM</sub>	100 dBμ. The pin 13 output	80	120	160	mVrms
S-meter output	V <sub>SM-FM(1)</sub>	0 dBμ. The pin 11 output	0	0.1	0.5	V
	V <sub>SM-FM(2)</sub>	100 dBμ. The pin 11 output	3.6	4.3	5.0	V
Muting attenuation	Mute-Att	100 dBμ, 100% mod. The pin 16 output	75	85		dB
[FM Stereo Characteristics] fc = 10.7 MHz, 100 dBμ, fm = 1 kHz, L + R = 90%, Pilot = 10%						
Separation (left)	Sep <sub>L</sub>	L mod. The pin 16 output/pin 17 output	30	45		dB
Separation (right)	Sep <sub>R</sub>	R mod. The pin 17 output/pin 16 output	30	45		dB
Stereo on level	ST <sub>ON</sub>	The pilot modulation such that V7 is under 0.7 V.	1.3	2.7	5	%
Stereo off level	ST <sub>OFF</sub>	The pilot modulation such that V7 is over 4.5 V.		1.5		%
Total harmonic distortion (main)	THD-main	L+R mod. The pin 16 output		0.3	1.3	%
Adjacent channel interference rejection ratio	Brej-3rd	fs = 113 kHz, Vs = 90 %, Pilot = 10 %, The pin 16 output vs. the L-R mod. 1 kHz demodulated output		40		dB
	Brej-5th	fs = 189 kHz, Vs = 90 %, Pilot = 10 %, The pin 16 output vs. the L-R mod. 1 kHz demodulated output		40		dB
[AM Characteristics] fc = 1000 kHz, fm = 1 kHz						
Current drain	I <sub>CCO-AM</sub>	With no input	15	25	35	mA
Detector output	V <sub>OAM(1)</sub>	23 dBμ, 30% mod. The pin 16 output	100	180	360	mVrms
	V <sub>OAM(2)</sub>	80 dBμ, 30% mod. The pin 16 output	200	320	500	mVrms
Signal-to-noise ratio	S/N <sub>AM(1)</sub>	23 dBμ, 30% mod. The pin 16 output	18	22		dB
	S/N <sub>AM(2)</sub>	80 dBμ, 30% mod. The pin 16 output	49	55		dB
Total harmonic distortion (mono)	THD <sub>AM(1)</sub>	80 dBμ, 30% mod. The pin 16 output		0.4	1.2	%
	THD <sub>AM(2)</sub>	80 dBμ, 80% mod. The pin 16 output		1.0	4.0	%
LED sensitivity	SD <sub>On-AM</sub>		17	27	37	dBμ
Local oscillator buffer output	V <sub>OSC-AM</sub>	With no input. The pin 30 output	110	160	220	mVrms
IF counter buffer output	V <sub>IFBuff-AM</sub>	80 dBμ, no modulation. The pin 13 output	160	220	300	mVrms
ST-IF output	V <sub>STIF-AM</sub>	80 dBμ, no modulation. The pin 7 output	16	34	48	mVrms
S-meter output	V <sub>SM-AM</sub>	0 dBμ, no modulation.	0	0	0.2	V



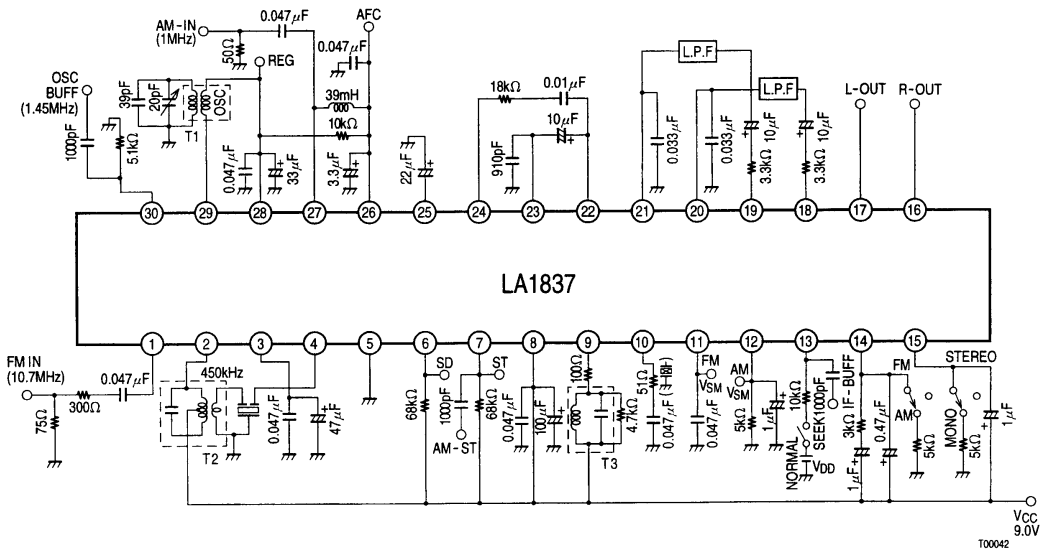
No. 5688-2/9

LA1837

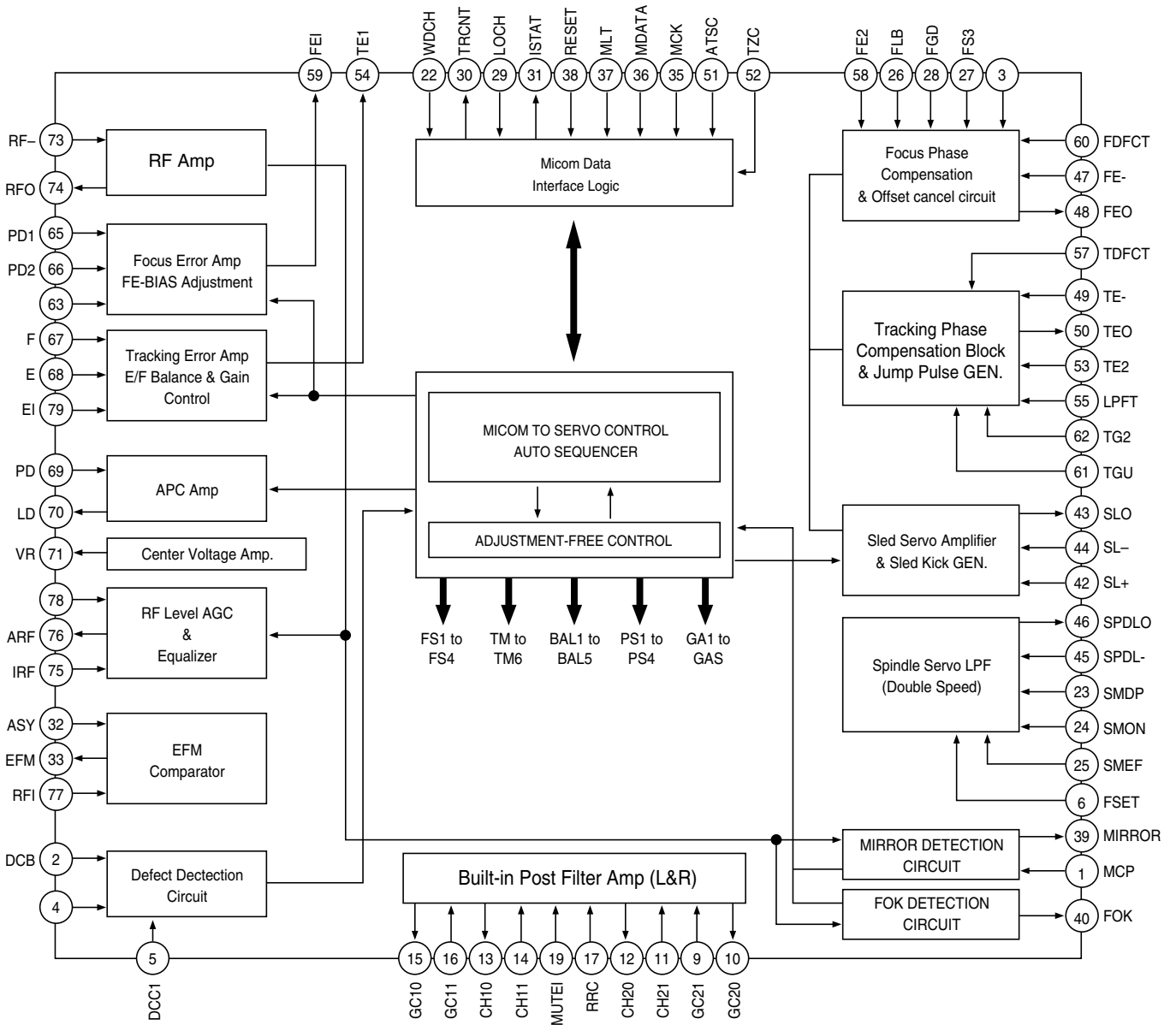
Block Diagram



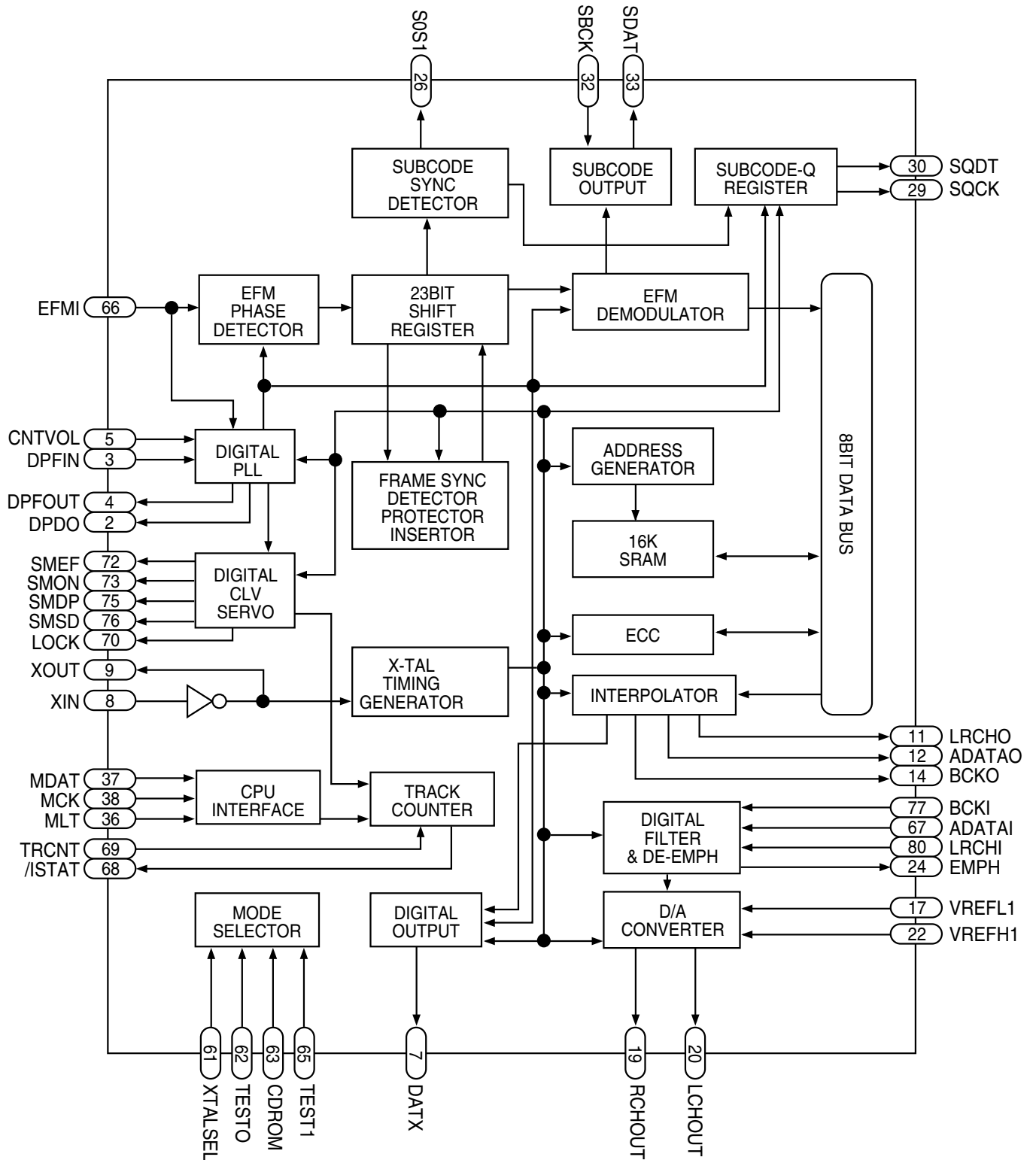
Test Circuit Diagram



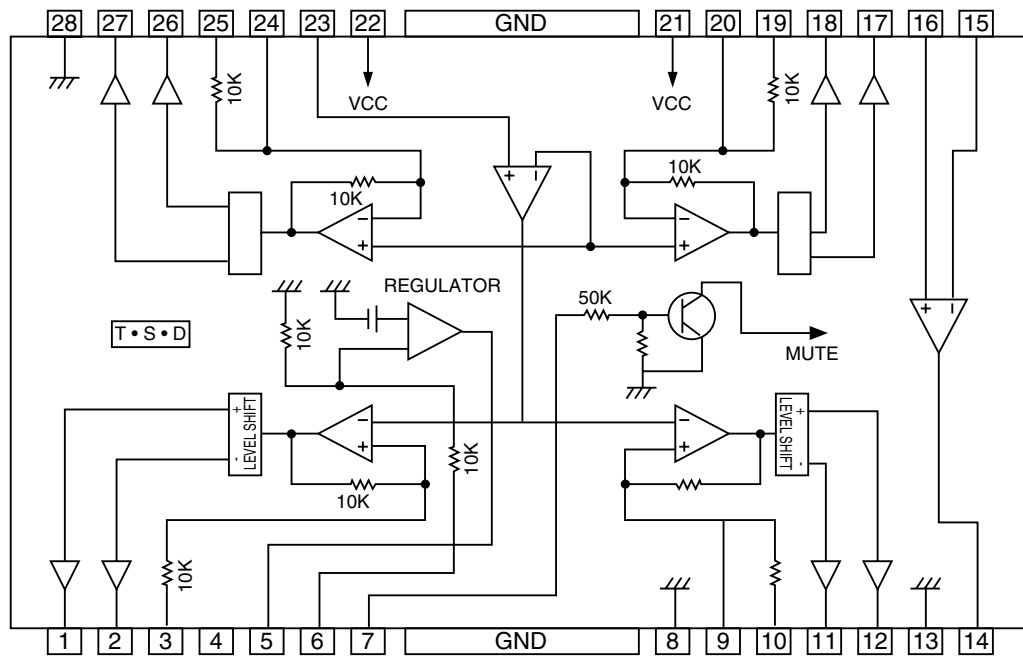
10-6 KB9223



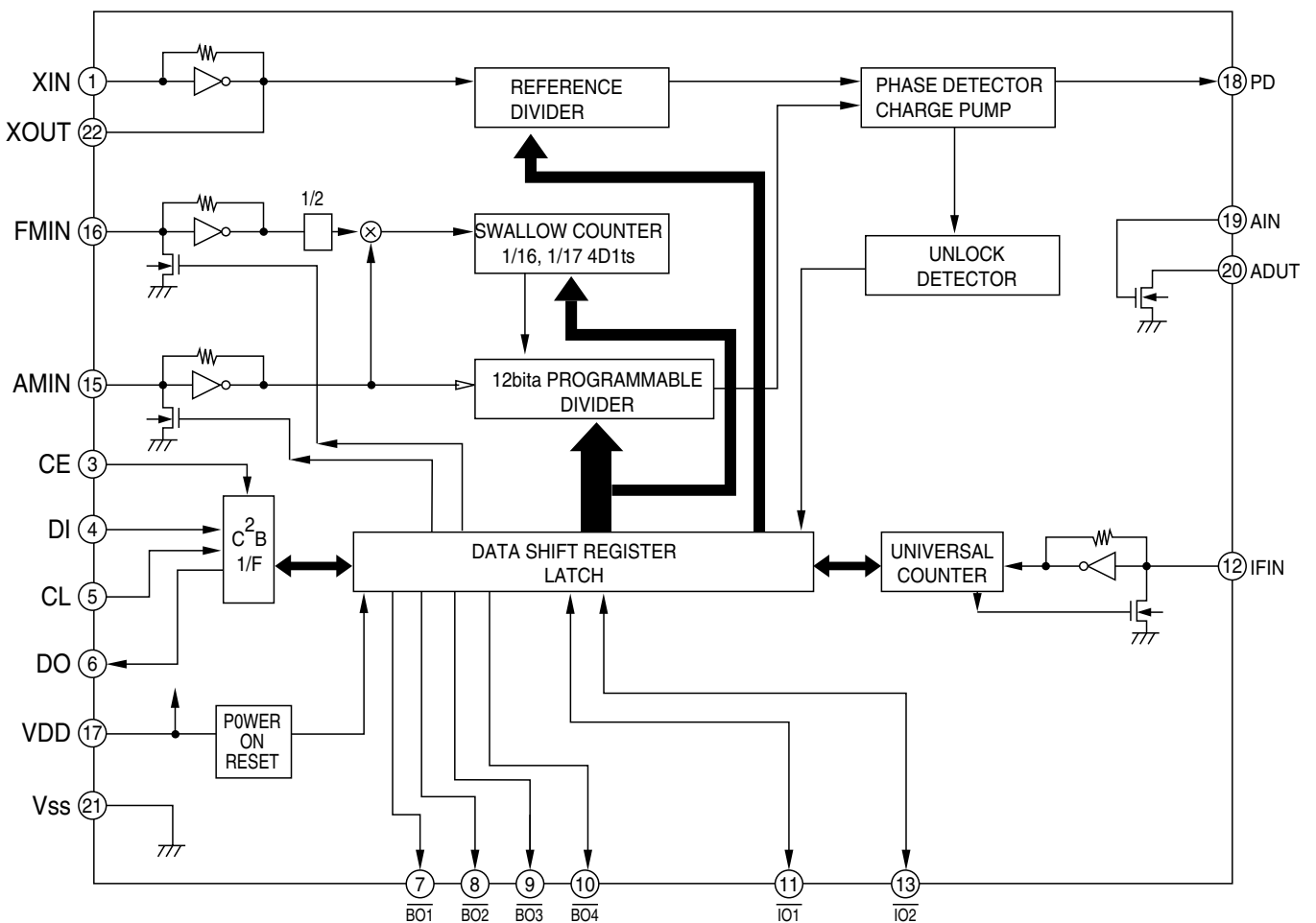
IC Internal Diagram  
**10-7 KS9286**



### 10-8 KA9258

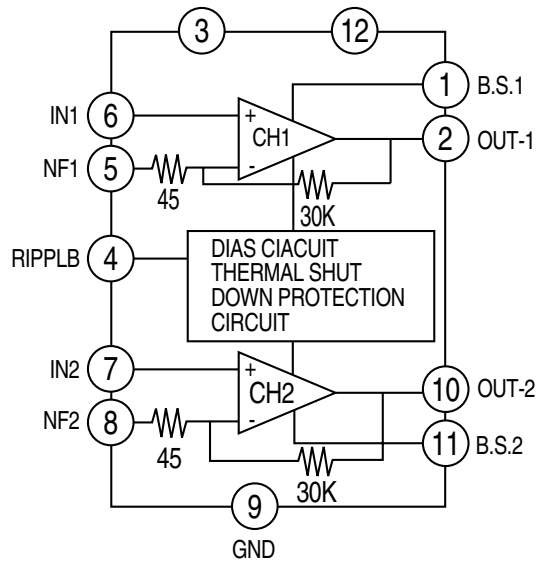


### 10-9 LC72131 ; HIC1

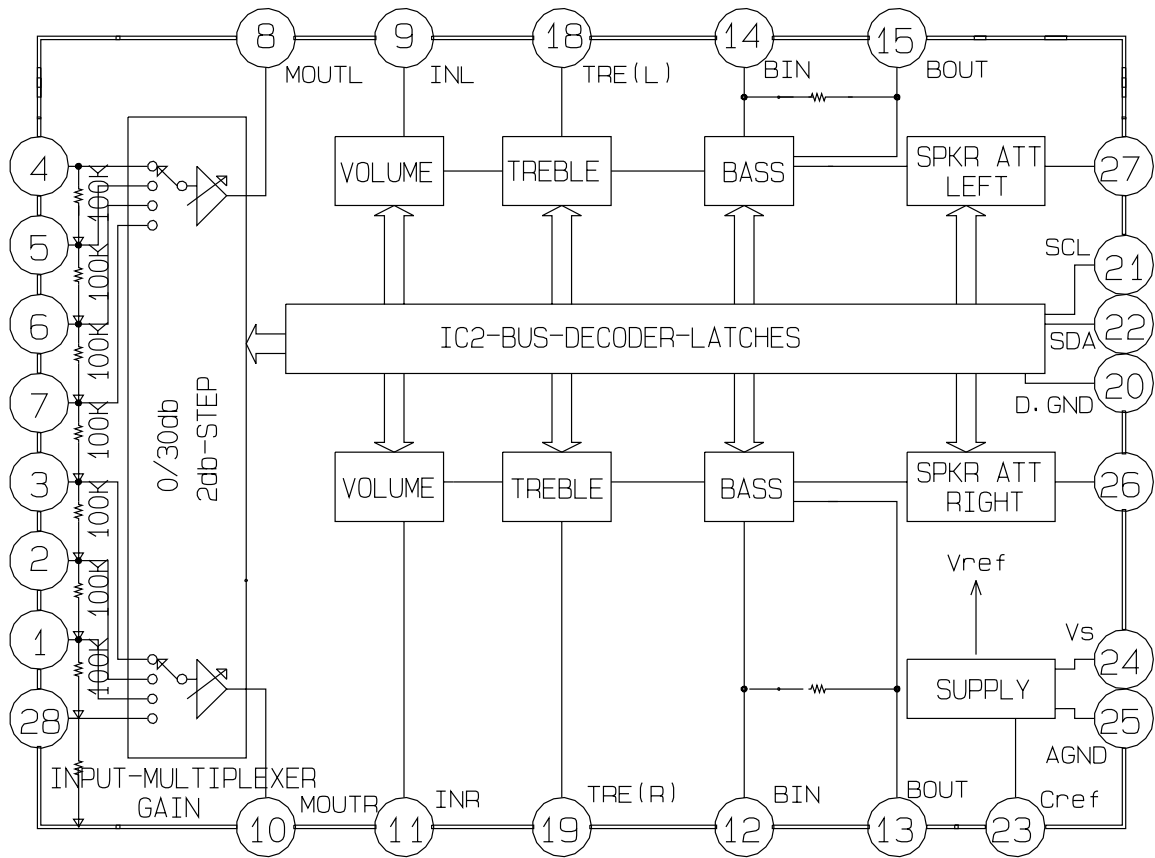


**10-10 TA8207K ; AIC1**

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10-11 TDA7440D ; JIC1





**10-12 MP3 MICOM ; AUIC3,OKI ,MSM66573 (TQFP100)**

No	PORT	I/O	PIN ASSIGN	No	PORT	I/O	PIN ASSIGN
1	P10-4	O	GO	51	P0-0	I/O	M-ERR
2	P10-5	O		52	P0-1	I/O	M-A/F
3	P10-7	O	wingo ce	53	P0-2	I/O	M-STROBE
4	P8-0	I/O	data I/o	54	P0-3	I/O	E-SEL/IN
5	P8-1	O		55	P0-4	I/O	E-INIT
6	P8-2	I/O	clock	56	P0-5	I/O	E-ERR
7	P8-3	O		57	P0-6	I/O	E-A/F
8	P8-4	I		58	P0-7	I/O	E-STROBE
9	P8-6	O		59		\$	Vss
10	P8-7	O		60	P4-0	I/O	E-DATA 1
11	P7-6	O		61	P4-1	I/O	E-DATA 2
12	P7-7	O		62	P4-2	I/O	E-DATA 3
13			VDD	63	P4-3	I/O	E-DATA 4
14			Vss	64	P4-4	I/O	E-DATA 5
15	P9-7	O		65	P4-5	I/O	E-DATA 6
16	P9-0	INT		66	P4-6	I/O	E-DATA 7
17	P9-1	INT		67	P4-7	I/O	E-DATA 8
18	P9-2	O		68	P1-0	O	M-DATA 1
19	P9-3	O		69	P1-1	O	M-DATA 2
20	P6-0	INT	wingo request	70	P1-2	O	M-DATA 3
21	P6-1	INT		71	P1-3	O	M-DATA 4
22	P6-2	INT		72	P1-4	O	M-DATA 5
23	P6-3	INT		73	P1-5	O	M-DATA 6
24	P6-4	I/O	E-P/CONTROL	74	P1-6	O	M-DATA 7
25	P6-5	I/O	E-BLANK	75	P1-7	O	M-DATA 8
26	P6-6	I/O		76	P2-0	O	e-data/IN1
27	P6-7	I/O		77	P2-1	O	e-data/out1
28	P5-4	I/O		78	P2-2	O	e-data/OUT2
29	P5-5	I/O		79	P2-3	O	e-data/in2
30	P5-6	I/O		80		\$	Vdd
31	P5-7	I/O		81		\$	Vref(VDD)
32	/RESET IN	I	RST_IN	82	A/D P12-0	I	
33	NM1		NMI(VCC)	83	A/D P12-1	I	
34	/EA		/EA(VDD)	84	A/D P12-2	I	
35			VDD	85	A/D P12-3	I	
36			XT0(GND)	86	A/D P12-4	I	
37			XTI	87	A/D P12-5	I	
38			GND	88	A/D P12-6	I	
39			OSC0	89	A/D P12-7	I	
40			OSCI	90		\$	Vss
41			VDD	91	P7-0	O	
42	P11-0	O		92	P7-1	O	
43	P11-1	O		93		\$	Vss
44	P11-2	O		94	P7-2	O	
45	P11-3	O		95	P7-4	O	
46	P11-6	O		96	P7-5	O	
47	P11-7	O		97	P10-0	I/O	CLK
48	P3-1	I		98	P10-1	O	DATA OUT
49	P3-2	I		99	P10-2	I	DATA IN
50	P3-3	O		100	P10-3	O	DIR

## 10-13 SLIM MICOM ; UIC3

PORT	NO	NAME	IO	FUNCTION	PORT	NO	NAME	IO	FUNCTION
P 16	1	MCE		MYMP3 CE	VP	51	VPP	O	-32V
P 17	2	POWER	O	POWER ON/OFF	S20	52		O	VFD SEG 14 RDS
P3 0	3	MO/ST	I	FM MONO/STEREO	S21	53		O	VFD SEG 13 BACKUP
1	4	TUNED	I	TUNED	S22	54		O	VFD SEG 12
2	5	A/MU	O	AUDIO MUTE	S23	55		O	VFD SEG 11
3	6	DI	I	LC72131 DI( PCB->DO )	S24	56	UD8	O	VFD SEG 10
4	7	CE	O	LC72131 CHIP ENABLE	S25	57	UD7	O	VFD SEG 9
5	8	CLK	O	LC72131/TDA7440 CLOCK	S26	58	UD6	O	VFD SEG 8
6	9	DO	O	LC72-/TDA- DO (PCB->DI)	S27	59	UD5	O	VFD SEG 7
7	10	RDS	I	RDS DATA OUT	S28	60	UD4	O	VFD SEG 6
P70	11	RDS	I	RDS CLK	S29	61	UD3	O	VFD SEG 5
RES	12	RESET	I	MICOM RESET	S30	62	UD2	O	VFD SEG 4
P74	13	CF1	I	32.768 KHz IN	S31	63	UD1	O	VFD SEG 3
P75	14	CF2	O	32.768KHz OUT	S32	64		O	VFD SEG 2
VSS	15	GND		GND	S33	65	S18	O	VFD SEG 1
CF 1	16	XT1	I	6MHZ OSC IN	2	66	OPT	I	OPTION INPUT
CF 2	17	XT2	O	6MHZ OSC OUT	3	67		I	
VDD1	18	VDD		VDD	4	68		I	
P8 0	19	LKFS	I	LKFS	5	69		I	
1	20	FOK	I	FOK	6	70		I	
2	21	TRCNT	I	TRCNT	7	71		I	
3	22	SENSE	I	SENSE & LIMIT SW	VDD4	72	VDD4		VFD 5V
4	23	AD1	I	KEY A/D 1	PF 0	73			
5	24	AD2	I	KEY A/D 2	1	74			
6	25	AD3	I	KEY A/D 3	2	75	CLOSM	O	CLOSE MOTOR
7	26	CHARG	I	CHARGBE BATTERY	3	76	OPENM	O	OPEN MOTOR
P7 1	27	SCE	I	MYMP3 REQUEST	4	77	PLED	O	P SOUND LED
2	28	SCOR	I	SCOR IN	5	78	EQLED	O	EQ LED
3	29	REMO	I	REMOCON IN	6	79	SNLED	O	SNOOE LED
S 0	30	S0	O		7	80	T_CNT		TUNER/CD CONTROL
1	31	S1	O		PG 0	81		O	
2	32	S2	O		1	82		O	
3	33	S3	O		2	83		O	
4	34	S4	O	VFD GRID 1	3	84		O	
5	35	S5	O	VFD GRID 2	PO 0	85	MCK	O	MCK
6	36	S6	O	VFD GRID 3	1	86	XRST	O	RESET
7	37	S7	O	VFD GRID 4	2	87	MDATA	O	DATA
8	38	S8	O	VFD GRID 5	3	88	MLT	O	MLT
9	39	S9	O	VFD GRID 6	VSS2	89			GND
10	40	S10	O	VFD GRID 7	VDD2	90			VDD2
11	41	S11	O	VFD GRID 8	P0 4	91	MY-S	I	"MY-MY,MP3 SENSE"
12	42	S12	O		5	92	P_SEN	I	POWER SENSE
13	43	S13	O		6	93	SW2	I	CLOSE SW
14	44	S14	O		7	94	SW1	I	OPEN SW
15	45	S15	O		P1 0	95	CMUTE	O	CD MUTE
VDD3	46	VDD3		VFD 5V	1	96	SQDT	I	SUBQ DATA IN
S 16	47	S16	O	VFD SEG18 TUNER	2	97	SQCK	O	SUBQ DATA IN CLK
17	48	S17	O	VFD SEG17 TUNER	3	98	PCTL	O	P-SOUND ON/OFF
18	49	S18	O	VFD SEG16 TUNER	4	99	SDADA	I/O	SYTEM DATA I/O
19	50	S19	O	VFD SEG15 12/24 HR	5	100	SCLOCK	O	SYSTEM CLOCK